



# Modbus Register Map: Galaxy RPP

990-6382  
14 August 2020

Notes:

- 16-bit registers are transmitted MSB first (i.e. big-endian).
- INT32 and UINT32 are most-significant word in n+0, least significant word in n+1 (i.e. big-endian).
- Function code 3 is supported.
- Modbus serial RTU and Modbus TCP is supported.
- Signed numbers are twos-compliment
- Status bits are atomic within a single Modbus register. User should not look for consistency across multiple registers, only within a single register.
- For ASCII strings less than the maximum length, the unused characters are filled with nulls.
- Single-register reads of reserved or undefined registers will return an error. Block reads which begin with a valid register will not return an error but will return zeros for undefined registers.
- Strings are two characters per register, first character in high-order byte, second character in low-order byte. Printable ASCII only.
- Bit #0 is least significant bit.
- Data Type column: "INT16"=signed 16-bit integer, "UINT16" = unsigned 16-bit integer, "INT32" = signed 32-bit integer, "UINT32" = unsigned 32-bit integer, "ENUM" is a UINT16 value which maps to a defined list of states, "ASCII" = the printable ASCII subset from 0x20 -0x7E. BOOLEAN= a single bit, 0 or 1.
- "Absolute Starting Register Address" = 0 (the column heading used in this table) is equivalent to "Register 40001" in Modicon terminology, which is address zero when transmitted over the wire.

For detailed Modbus configuration settings, please refer to the display.

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
	0	0	<b>1. General</b>										
40001	0	0		NA	RESERVED				1				
40002	1	1		RO	Display/NMC Model Number				9	ASCII			character string
40011	A	10		RO	Display/NMC Serial Number				8	ASCII			character string
40019	12	18		RO	Display/NMC Firmware Revision APP				9	ASCII			character string
40028	1B	27		RO	Display/NMC Hardware Revision				9	ASCII			character string
40037	24	36		RO	Display/NMC Date of Manufacture				6	ASCII			character string
40043	2A	42		RO	Product Name				20	ASCII			Remote Power Panel
40063	3E	62		RO	Product Model Number				11	ASCII			
40074	49	73		RO	Product Serial Number				11	ASCII			
40085	54	84		RO	Meter QTY				1	UINT16			0, 1, 2, 3
40086	55	85		RO	System Protection QTY				1	UINT16			0, 1, 2
40087	56	86			RESERVED				21				
	6B	107											
	<b>3E8</b>	<b>1000</b>	<b>2. Input 1</b>										
	<b>3E8</b>	<b>1000</b>	<b>2.1 Alarm/Status Registers</b>										
41001	3E8	1000		RO	Voltage Alarm/Status Register				1				
			0		Voltage Maximum alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			1		Voltage Minimum alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			2		Voltage High alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			3		Voltage Low alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			4		Voltage Maximum alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
			5		Voltage Minimum alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			6		Voltage High alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			7		Voltage Low alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			8		Voltage Maximum alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			9		Voltage Minimum alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			10		Voltage High alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			11		Voltage Low alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			12		NA					BOOLEAN			
			13		NA					BOOLEAN			
			14		NA					BOOLEAN			
			15		Transient Voltage Surge Suppressor Alarm					BOOLEAN			0 = No Alarm 1 = Alarm Active
41002	3E9	1001		RO	Current Alarm/Status Register				1				
			0		Current Maximum alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			1		Current Minimum alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			2		Current High alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			3		Current Low alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			4		Current Maximum alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			5		Current Minimum alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			6		Current High alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			7		Current Low alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			8		Current Maximum alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			9		Current Minimum alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			10		Current High alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			11		Current Low alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			12		NA					BOOLEAN			
			13		NA					BOOLEAN			
			14		NA					BOOLEAN			
			15		NA					BOOLEAN			
41003	3EA	1002		RO	General Alarm/Status Register				1				
			0		Communication lost					BOOLEAN			0=Com OK 1=Com Lost
			1		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			2		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
			3		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			4		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			5		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			6		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			7		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			8		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			9		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			10		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			11		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			12		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			13		Input Breaker Tripped Alarm					BOOLEAN			0 = No Alarm 1 = Alarm Active
			14		Input Breaker Open Alarm					BOOLEAN			0 = No Alarm 1 = Alarm Active
			15		NA					BOOLEAN			
41004	3EB	1003		RO	Alarm Configuration Register				1				
			0		Voltage Maximum alarm enabled					BOOLEAN			1=Enabled
			1		Voltage Minimum alarm enabled					BOOLEAN			1=Enabled
			2		Voltage High alarm enabled					BOOLEAN			1=Enabled
			3		Voltage Low alarm enabled					BOOLEAN			1=Enabled
			4		Current Maximum alarm enabled					BOOLEAN			1=Enabled
			5		Current Minimum alarm enabled					BOOLEAN			1=Enabled
			6		Current High alarm enabled					BOOLEAN			1=Enabled
			7		Current Low alarm enabled					BOOLEAN			1=Enabled
			8		RESERVED					BOOLEAN			
			9		RESERVED					BOOLEAN			
			10		RESERVED					BOOLEAN			
			11		RESERVED					BOOLEAN			
			12		Input Breaker Open Alarm Enabled					BOOLEAN			1=Enabled
			13		RESERVED					BOOLEAN			
			14		RESERVED					BOOLEAN			
			15		RESERVED					BOOLEAN			1=Enabled
	3EC	1004											
	<b>41A</b>	<b>1050</b>	<b>2.2 Configuration</b>										
41051	41A	1050		RO	Input Nominal Frequency				1	UINT16			Hz
41052	41B	1051		RO	Input Nominal Voltage				1	UINT16			Volts

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41053	41C	1052		RO	Voltage Minimum Alarm Threshold				1	UINT16			%
41054	41D	1053		RO	Voltage Low Alarm Threshold				1	UINT16			%
41055	41E	1054		RO	Voltage High Alarm Threshold				1	UINT16			%
41056	41F	1055		RO	Voltage Maximum Alarm Threshold				1	UINT16			%
41057	420	1056		RO	Current Minimum Alarm Threshold				1	UINT16			%
41058	421	1057		RO	Current Low Alarm Threshold				1	UINT16			%
41059	422	1058		RO	Current High Alarm Threshold				1	UINT16			%
41060	423	1059		RO	Current Maximum Alarm Threshold				1	UINT16			%
	424	1060											
	<b>44C</b>	<b>1100</b>	<b>2.3 Measurements</b>										
41101	44C	1100		RO	Frequency				1	UINT16		100	Hz
41102	44D	1101		RO	Voltage L1-2				1	UINT16		10	Volts
41103	44E	1102		RO	Voltage L2-3				1	UINT16		10	Volts
41104	44F	1103		RO	Voltage L3-1				1	UINT16		10	Volts
41105	450	1104		RO	Voltage L-L Avg				1	UINT16		10	Volts
41106	451	1105		RO	Voltage L1-N				1	UINT16		10	Volts
41107	452	1106		RO	Voltage L2-N				1	UINT16		10	Volts
41108	453	1107		RO	Voltage L3-N				1	UINT16		10	Volts
41109	454	1108		RO	Voltage L-N Avg				1	UINT16		10	Volts
41110	455	1109		RO	Current L1				1	UINT16		10	Amperes
41111	456	1110		RO	Current L2				1	UINT16		10	Amperes
41112	457	1111		RO	Current L3				1	UINT16		10	Amperes
41113	458	1112		RO	Current N				1	UINT16		10	Amperes
41114	459	1113		RO	RESERVED				1	UINT16			
41115	45A	1114		RO	Current Avg				1	UINT16		10	Amperes
41116	45B	1115		RO	Active Power L1				1	UINT16		100	kW
41117	45C	1116		RO	Active Power L2				1	UINT16		100	kW
41118	45D	1117		RO	Active Power L3				1	UINT16		100	kW
41119	45E	1118		RO	Active Power Total				1	UINT16		100	kW
41120	45F	1119		RO	RESERVED				1	UINT16		100	KVAR
41121	460	1120		RO	RESERVED				1	UINT16		100	KVAR
41122	461	1121		RO	RESERVED				1	UINT16		100	KVAR
41123	462	1122		RO	RESERVED				1	UINT16		100	KVAR
41124	463	1123		RO	Apparent Power L1				1	UINT16		100	kVA
41125	464	1124		RO	Apparent Power L2				1	UINT16		100	kVA
41126	465	1125		RO	Apparent Power L3				1	UINT16		100	kVA
41127	466	1126		RO	Apparent Power Total				1	UINT16		100	kVA
41128	467	1127		RO	Power Factor L1				1	INT16		100	

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41129	468	1128		RO	Power Factor L2				1	INT16		100	
41130	469	1129		RO	Power Factor L3				1	INT16		100	
41131	46A	1130		RO	Power Factor Total				1	INT16		100	
41132	46B	1131		RO	Accumulated Energy Reset Date/Time				10	ASCII			
41142	475	1141		RO	Active Energy Delivered (Into Load)				2	INT32		10	kVAh
41144	477	1143		RO	Maximum Instantaneous Current L1				1	UINT16		10	Amperes
41145	478	1144		RO	Maximum Instantaneous Current L2				1	UINT16		10	Amperes
41146	479	1145		RO	Maximum Instantaneous Current L3				1	UINT16		10	Amperes
41147	47A	1146		RO	Maximum Instantaneous Current N				1	UINT16		10	Amperes
41148	47B	1147		RO	Current Phase Angle L1				1	INT16		10	
41149	47C	1148		RO	Current Phase Angle L2				1	INT16		10	
41150	47D	1149		RO	Current Phase Angle L3				1	INT16		10	
41151	47E	1150		RO	Energy Usage Accumulated Total (breaker)				2	UINT32		10	kVAh
41153	480	1152		RO	Current THD L1				1	INT16		10	Amperes
41154	481	1153		RO	Current THD L2				1	INT16		10	Amperes
41155	482	1154		RO	Current THD L3				1	INT16		10	Amperes
41156	483	1155		RO	RESERVED				1	UINT16		10	Amperes
41157	484	1156		RO	Voltage THD L1-2				1	INT16		10	Volts
41158	485	1157		RO	Voltage THD L2-3				1	INT16		10	Volts
41159	486	1158		RO	Voltage THD L3-1				1	INT16		10	Volts
41160	487	1159		RO	Voltage THD L1-N				1	INT16		10	Volts
41161	488	1160		RO	Voltage THD L2-N				1	INT16		10	Volts
41162	489	1161		RO	Voltage THD L3-N				1	INT16		10	Volts
	48A	1162											
	<b>7D0</b>	<b>2000</b>											
	<b>7D0</b>	<b>2000</b>	<b>3. Input 2</b>										
			<b>3.1 Alarm/Status Registers</b>										
42001	7D0	2000		RO	Voltage Alarm/Status Register				1				
			0		Voltage Maximum alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			1		Voltage Minimum alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			2		Voltage High alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			3		Voltage Low alarm L1-2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			4		Voltage Maximum alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			5		Voltage Minimum alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			6		Voltage High alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			7		Voltage Low alarm L2-3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			8		Voltage Maximum alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
			9		Voltage Minimum alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			10		Voltage High alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			11		Voltage Low alarm L3-1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			12		NA					BOOLEAN			
			13		NA					BOOLEAN			
			14		NA					BOOLEAN			
			15		Transient Voltage Surge Suppressor Alarm					BOOLEAN			
42002	7D1	2001		RO	Current Alarm/Status Register				1				
			0		Current Maximum alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			1		Current Minimum alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			2		Current High alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			3		Current Low alarm L1					BOOLEAN			0 = No Alarm 1 = Alarm Active
			4		Current Maximum alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			5		Current Minimum alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			6		Current High alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			7		Current Low alarm L2					BOOLEAN			0 = No Alarm 1 = Alarm Active
			8		Current Maximum alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			9		Current Minimum alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			10		Current High alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			11		Current Low alarm L3					BOOLEAN			0 = No Alarm 1 = Alarm Active
			12		NA					BOOLEAN			
			13		NA					BOOLEAN			
			14		NA					BOOLEAN			
			15		NA					BOOLEAN			
42003	7D2	2002		RO	General Alarm/Status Register				1				
			0		Communication lost					BOOLEAN			0=Com OK 1=Com Lost
			1		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			2		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			3		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			4		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			5		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			6		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
			7		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			8		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			9		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			10		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			11		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			12		RESERVED					BOOLEAN			0 = No Alarm 1 = Alarm Active
			13		Input Breaker Tripped Alarm					BOOLEAN			0 = No Alarm 1 = Alarm Active
			14		Input Breaker Open Alarm					BOOLEAN			0 = No Alarm 1 = Alarm Active
			15		NA					BOOLEAN			
42004	7D3	2003		RO	Alarm Configuration Register				1				
			0		Voltage Maximum Alarm Enabled					BOOLEAN			1=Enable
			1		Voltage Minimum Alarm Enabled					BOOLEAN			1=Enable
			2		Voltage High Alarm Enabled					BOOLEAN			1=Enable
			3		Voltage Low Alarm Enabled					BOOLEAN			1=Enable
			4		Current Maximum Alarm Enable					BOOLEAN			1=Enable
			5		Current Minimum Alarm Enabled					BOOLEAN			1=Enable
			6		Current High Alarm Enabled					BOOLEAN			1=Enable
			7		Current Low Alarm Enabled					BOOLEAN			1=Enable
			8		RESERVED					BOOLEAN			1=Enable
			9		RESERVED					BOOLEAN			1=Enable
			10		RESERVED					BOOLEAN			1=Enable
			11		RESERVED					BOOLEAN			1=Enable
			12		Input Breaker Open Alarm Enabled					BOOLEAN			1=Enable
			13		RESERVED					BOOLEAN			
			14		RESERVED					BOOLEAN			
			15		RESERVED					BOOLEAN			
	7D4	2004											
	<b>802</b>	<b>2050</b>	<b>3.2 Configuration</b>										
42051	802	2050		RO	Input Nominal Frequency				1	UINT16			Hz
42052	803	2051		RO	Input Nominal Voltage				1	UINT16			Volts
42053	804	2052		RO	Voltage Minimum Alarm Threshold				1	UINT16			%
42054	805	2053		RO	Voltage Low Alarm Threshold				1	UINT16			%
42055	806	2054		RO	Voltage High Alarm Threshold				1	UINT16			%
42056	807	2055		RO	Voltage Maximum Alarm Threshold				1	UINT16			%

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
42057	808	2056		RO	Current Minimum Alarm Threshold				1	UINT16			%
42058	809	2057		RO	Current Low Alarm Threshold				1	UINT16			%
42059	80A	2058		RO	Current High Alarm Threshold				1	UINT16			%
42060	80B	2059		RO	Current Maximum Alarm Threshold				1	UINT16			%
	80C	2060											
	<b>834</b>	<b>2100</b>	<b>3.3 Measurements</b>										
42101	834	2100		RO	Frequency				1	UINT16		100	Hz
42102	835	2101		RO	Voltage L1-2				1	UINT16		10	Volts
42103	836	2102		RO	Voltage L2-3				1	UINT16		10	Volts
42104	837	2103		RO	Voltage L3-1				1	UINT16		10	Volts
42105	838	2104		RO	Voltage L-L Avg				1	UINT16		10	Volts
42106	839	2105		RO	Voltage L1-N				1	UINT16		10	Volts
42107	83A	2106		RO	Voltage L2-N				1	UINT16		10	Volts
42108	83B	2107		RO	Voltage L3-N				1	UINT16		10	Volts
42109	83C	2108		RO	Voltage L-N Avg				1	UINT16		10	Volts
42110	83D	2109		RO	Current L1				1	UINT16		10	Amperes
42111	83E	2110		RO	Current L2				1	UINT16		10	Amperes
42112	83F	2111		RO	Current L3				1	UINT16		10	Amperes
42113	840	2112		RO	Current N				1	UINT16		10	Amperes
42114	841	2113		RO	RESERVED				1	UINT16			
42115	842	2114		RO	Current Avg				1	UINT16		10	Amperes
42116	843	2115		RO	Active Power L1				1	UINT16		100	kW
42117	844	2116		RO	Active Power L2				1	UINT16		100	kW
42118	845	2117		RO	Active Power L3				1	UINT16		100	kW
42119	846	2118		RO	Active Power Total				1	UINT16		100	kW
42120	847	2119		RO	RESERVED				1	UINT16		100	kVAR
42121	848	2120		RO	RESERVED				1	UINT16		100	kVAR
42122	849	2121		RO	RESERVED				1	UINT16		100	kVAR
42123	84A	2122		RO	RESERVED				1	UINT16		100	kVAR
42124	84B	2123		RO	Apparent Power L1				1	UINT16		100	kVA
42125	84C	2124		RO	Apparent Power L2				1	UINT16		100	kVA
42126	84D	2125		RO	Apparent Power L3				1	UINT16		100	kVA
42127	84E	2126		RO	Apparent Power Total				1	UINT16		100	kVA
42128	84F	2127		RO	Power Factor L1				1	INT16		100	
42129	850	2128		RO	Power Factor L2				1	INT16		100	
42130	851	2129		RO	Power Factor L3				1	INT16		100	
42131	852	2130		RO	Power Factor Total				1	INT16		100	
42132	853	2131		RO	Accumulated Energy Reset Date/Time				10	ASCII			
42142	85D	2141		RO	Active Energy Delivered (Into Load)				2	INT32		10	kVAh



Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
42144	85F	2143		RO	Maximum Instantaneous Current L1				1	UINT16		10	Amperes
42145	860	2144		RO	Maximum Instantaneous Current L2				1	UINT16		10	Amperes
42146	861	2145		RO	Maximum Instantaneous Current L3				1	UINT16		10	Amperes
42147	862	2146		RO	Maximum Instantaneous Current N				1	UINT16		10	Amperes
42148	863	2147		RO	Current Phase Angle L1				1	INT16		10	
42149	864	2148		RO	Current Phase Angle L2				1	INT16		10	
42150	865	2149		RO	Current Phase Angle L3				1	INT16		10	
42151	866	2150		RO	Energy Usage Accumulated Total (breaker)				2	UINT32		10	kVAh
42153	868	2152		RO	Current THD L1				1	INT16		10	Amperes
42154	869	2153		RO	Current THD L2				1	INT16		10	Amperes
42155	86A	2154		RO	Current THD L3				1	INT16		10	Amperes
42156	86B	2155		RO	RESERVED				1	UINT16		10	Amperes
42157	86C	2156		RO	Voltage THD L1-2				1	INT16		10	Volts
42158	86D	2157		RO	Voltage THD L2-3				1	INT16		10	Volts
42159	86E	2158		RO	Voltage THD L3-1				1	INT16		10	Volts
42160	86F	2159		RO	Voltage THD L1-N				1	INT16		10	Volts
42161	870	2160		RO	Voltage THD L2-N				1	INT16		10	Volts
42162	871	2161		RO	Voltage THD L3-N				1	INT16		10	Volts
	872	2162											
	<b>BB8</b>	<b>3000</b>	<b>4. Branches</b>										
	<b>BB8</b>	<b>3000</b>	<b>4.1 Configuration</b>										
43821	EEC	3820		RO	Load Identifier	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	1640	ASCII			
43821	EEC	3820				1	1 (P1)	1	10	ASCII			character string Eg: AC Line, nosupport = if MCM unsupported
43831	EF6	3830				3	3 (P1)	3	10	ASCII			
43841	F00	3840				5	5 (P1)	5	10	ASCII			
43851	F0A	3850				7	7 (P1)	7	10	ASCII			
43861	F14	3860				9	9 (P1)	9	10	ASCII			
43871	F1E	3870				11	11 (P1)	11	10	ASCII			
43881	F28	3880				13	13 (P1)	13	10	ASCII			
43891	F32	3890				15	15 (P1)	15	10	ASCII			
43901	F3C	3900				17	17 (P1)	17	10	ASCII			
43911	F46	3910				19	19 (P1)	19	10	ASCII			
43921	F50	3920				21	21 (P1)	21	10	ASCII			
43931	F5A	3930				23	23 (P1)	23	10	ASCII			
43941	F64	3940				25	25 (P1)	25	10	ASCII			
43951	F6E	3950				27	27 (P1)	27	10	ASCII			
43961	F78	3960				29	29 (P1)	29	10	ASCII			
43971	F82	3970				31	31 (P1)	31	10	ASCII			
43981	F8C	3980				33	33 (P1)	33	10	ASCII			
43991	F96	3990				35	35 (P1)	35	10	ASCII			
44001	FA0	4000				37	37 (P1)	37	10	ASCII			
44011	FAA	4010				39	39 (P1)	39	10	ASCII			
44021	FB4	4020				41	41 (P1)	41	10	ASCII			
44231	1086	4230				2	2 (P1)	2	10	ASCII			
44241	1090	4240				4	4 (P1)	4	10	ASCII			
44251	109A	4250				6	6 (P1)	6	10	ASCII			
44261	10A4	4260				8	8 (P1)	8	10	ASCII			
44271	10AE	4270				10	10 (P1)	10	10	ASCII			
44281	10B8	4280				12	12 (P1)	12	10	ASCII			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
44291	10C2	4290				14	14 (P1)	14	10	ASCII			
44301	10CC	4300				16	16 (P1)	16	10	ASCII			
44311	10D6	4310				18	18 (P1)	18	10	ASCII			
44321	10E0	4320				20	20 (P1)	20	10	ASCII			
44331	10EA	4330				22	22 (P1)	22	10	ASCII			
44341	10F4	4340				24	24 (P1)	24	10	ASCII			
44351	10FE	4350				26	26 (P1)	26	10	ASCII			
44361	1108	4360				28	28 (P1)	28	10	ASCII			
44371	1112	4370				30	30 (P1)	30	10	ASCII			
44381	111C	4380				32	32 (P1)	32	10	ASCII			
44391	1126	4390				34	34 (P1)	34	10	ASCII			
44401	1130	4400				36	36 (P1)	36	10	ASCII			
44411	113A	4410				38	38 (P1)	38	10	ASCII			
44421	1144	4420				40	40 (P1)	40	10	ASCII			
44431	114E	4430				42	42 (P1)	42	10	ASCII			
44641	1220	4640				NA	1 (P2)	43	10	ASCII			
44651	122A	4650				NA	3 (P2)	45	10	ASCII			
44661	1234	4660				NA	5 (P2)	47	10	ASCII			
44671	123E	4670				NA	7 (P2)	49	10	ASCII			
44681	1248	4680				NA	9 (P2)	51	10	ASCII			
44691	1252	4690				NA	11 (P2)	53	10	ASCII			
44701	125C	4700				NA	13 (P2)	55	10	ASCII			
44711	1266	4710				NA	15 (P2)	57	10	ASCII			
44721	1270	4720				NA	17 (P2)	59	10	ASCII			
44731	127A	4730				NA	19 (P2)	61	10	ASCII			
44741	1284	4740				NA	21 (P2)	63	10	ASCII			
44751	128E	4750				NA	23 (P2)	65	10	ASCII			
44761	1298	4760				NA	25 (P2)	67	10	ASCII			
44771	12A2	4770				NA	27 (P2)	69	10	ASCII			
44781	12AC	4780				NA	29 (P2)	71	10	ASCII			
44791	12B6	4790				NA	31 (P2)	73	10	ASCII			
44801	12C0	4800				NA	33 (P2)	75	10	ASCII			
44811	12CA	4810				NA	35 (P2)	77	10	ASCII			
44821	12D4	4820				NA	37 (P2)	79	10	ASCII			
44831	12DE	4830				NA	39 (P2)	81	10	ASCII			
44841	12E8	4840				NA	41 (P2)	83	10	ASCII			
45051	13BA	5050				NA	2 (P2)	44	10	ASCII			
45061	13C4	5060				NA	4 (P2)	46	10	ASCII			
45071	13CE	5070				NA	6 (P2)	48	10	ASCII			
45081	13D8	5080				NA	8 (P2)	50	10	ASCII			
45091	13E2	5090				NA	10 (P2)	52	10	ASCII			
45101	13EC	5100				NA	12 (P2)	54	10	ASCII			
45111	13F6	5110				NA	14 (P2)	56	10	ASCII			
45121	1400	5120				NA	16 (P2)	58	10	ASCII			
45131	140A	5130				NA	18 (P2)	60	10	ASCII			
45141	1414	5140				NA	20 (P2)	62	10	ASCII			
45151	141E	5150				NA	22 (P2)	64	10	ASCII			
45161	1428	5160				NA	24 (P2)	66	10	ASCII			
45171	1432	5170				NA	26 (P2)	68	10	ASCII			
45181	143C	5180				NA	28 (P2)	70	10	ASCII			
45191	1446	5190				NA	30 (P2)	72	10	ASCII			
45201	1450	5200				NA	32 (P2)	74	10	ASCII			
45211	145A	5210				NA	34 (P2)	76	10	ASCII			
45221	1464	5220				NA	36 (P2)	78	10	ASCII			
45231	146E	5230				NA	38 (P2)	80	10	ASCII			
45241	1478	5240				NA	40 (P2)	82	10	ASCII			
45251	1482	5250				NA	42 (P2)	84	10	ASCII			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
45461	1554	5460		RO	Associated Phase	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			0=unknown, 1 = L1, 2 = L2, 3 = L3, 0xFFFF = unsupported
45461	1554	5460				1	1 (P1)	1	1	UINT16			
45462	1555	5461				3	3 (P1)	3	1	UINT16			
45463	1556	5462				5	5 (P1)	5	1	UINT16			
45464	1557	5463				7	7 (P1)	7	1	UINT16			
45465	1558	5464				9	9 (P1)	9	1	UINT16			
45466	1559	5465				11	11 (P1)	11	1	UINT16			
45467	155A	5466				13	13 (P1)	13	1	UINT16			
45468	155B	5467				15	15 (P1)	15	1	UINT16			
45469	155C	5468				17	17 (P1)	17	1	UINT16			
45470	155D	5469				19	19 (P1)	19	1	UINT16			
45471	155E	5470				21	21 (P1)	21	1	UINT16			
45472	155F	5471				23	23 (P1)	23	1	UINT16			
45473	1560	5472				25	25 (P1)	25	1	UINT16			
45474	1561	5473				27	27 (P1)	27	1	UINT16			
45475	1562	5474				29	29 (P1)	29	1	UINT16			
45476	1563	5475				31	31 (P1)	31	1	UINT16			
45477	1564	5476				33	33 (P1)	33	1	UINT16			
45478	1565	5477				35	35 (P1)	35	1	UINT16			
45479	1566	5478				37	37 (P1)	37	1	UINT16			
45480	1567	5479				39	39 (P1)	39	1	UINT16			
45481	1568	5480				41	41 (P1)	41	1	UINT16			
45502	157D	5501				2	2 (P1)	2	1	UINT16			
45503	157E	5502				4	4 (P1)	4	1	UINT16			
45504	157F	5503				6	6 (P1)	6	1	UINT16			
45505	1580	5504				8	8 (P1)	8	1	UINT16			
45506	1581	5505				10	10 (P1)	10	1	UINT16			
45507	1582	5506				12	12 (P1)	12	1	UINT16			
45508	1583	5507				14	14 (P1)	14	1	UINT16			
45509	1584	5508				16	16 (P1)	16	1	UINT16			
45510	1585	5509				18	18 (P1)	18	1	UINT16			
45511	1586	5510				20	20 (P1)	20	1	UINT16			
45512	1587	5511				22	22 (P1)	22	1	UINT16			
45513	1588	5512				24	24 (P1)	24	1	UINT16			
45514	1589	5513				26	26 (P1)	26	1	UINT16			
45515	158A	5514				28	28 (P1)	28	1	UINT16			
45516	158B	5515				30	30 (P1)	30	1	UINT16			
45517	158C	5516				32	32 (P1)	32	1	UINT16			
45518	158D	5517				34	34 (P1)	34	1	UINT16			
45519	158E	5518				36	36 (P1)	36	1	UINT16			
45520	158F	5519				38	38 (P1)	38	1	UINT16			
45521	1590	5520				40	40 (P1)	40	1	UINT16			
45522	1591	5521				42	42 (P1)	42	1	UINT16			
45543	15A6	5542				NA	1 (P2)	43	1	UINT16			
45544	15A7	5543				NA	3 (P2)	45	1	UINT16			
45545	15A8	5544				NA	5 (P2)	47	1	UINT16			
45546	15A9	5545				NA	7 (P2)	49	1	UINT16			
45547	15AA	5546				NA	9 (P2)	51	1	UINT16			
45548	15AB	5547				NA	11 (P2)	53	1	UINT16			
45549	15AC	5548				NA	13 (P2)	55	1	UINT16			
45550	15AD	5549				NA	15 (P2)	57	1	UINT16			
45551	15AE	5550				NA	17 (P2)	59	1	UINT16			
45552	15AF	5551				NA	19 (P2)	61	1	UINT16			
45553	15B0	5552				NA	21 (P2)	63	1	UINT16			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
45554	15B1	5553				NA	23 (P2)	65	1	UINT16			
45555	15B2	5554				NA	25 (P2)	67	1	UINT16			
45556	15B3	5555				NA	27 (P2)	69	1	UINT16			
45557	15B4	5556				NA	29 (P2)	71	1	UINT16			
45558	15B5	5557				NA	31 (P2)	73	1	UINT16			
45559	15B6	5558				NA	33 (P2)	75	1	UINT16			
45560	15B7	5559				NA	35 (P2)	77	1	UINT16			
45561	15B8	5560				NA	37 (P2)	79	1	UINT16			
45562	15B9	5561				NA	39 (P2)	81	1	UINT16			
45563	15BA	5562				NA	41 (P2)	83	1	UINT16			
45584	15CF	5583				NA	2 (P2)	44	1	UINT16			
45585	15D0	5584				NA	4 (P2)	46	1	UINT16			
45586	15D1	5585				NA	6 (P2)	48	1	UINT16			
45587	15D2	5586				NA	8 (P2)	50	1	UINT16			
45588	15D3	5587				NA	10 (P2)	52	1	UINT16			
45589	15D4	5588				NA	12 (P2)	54	1	UINT16			
45590	15D5	5589				NA	14 (P2)	56	1	UINT16			
45591	15D6	5590				NA	16 (P2)	58	1	UINT16			
45592	15D7	5591				NA	18 (P2)	60	1	UINT16			
45593	15D8	5592				NA	20 (P2)	62	1	UINT16			
45594	15D9	5593				NA	22 (P2)	64	1	UINT16			
45595	15DA	5594				NA	24 (P2)	66	1	UINT16			
45596	15DB	5595				NA	26 (P2)	68	1	UINT16			
45597	15DC	5596				NA	28 (P2)	70	1	UINT16			
45598	15DD	5597				NA	30 (P2)	72	1	UINT16			
45599	15DE	5598				NA	32 (P2)	74	1	UINT16			
45600	15DF	5599				NA	34 (P2)	76	1	UINT16			
45601	15E0	5600				NA	36 (P2)	78	1	UINT16			
45602	15E1	5601				NA	38 (P2)	80	1	UINT16			
45603	15E2	5602				NA	40 (P2)	82	1	UINT16			
45604	15E3	5603				NA	42 (P2)	84	1	UINT16			
45625	15F8	5624		RO	Breaker Rating	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			Amperes, 0 = unknown 0xFFFF = unsupported
45625	15F8	5624				1	1 (P1)	1	1	UINT16			
45626	15F9	5625				3	3 (P1)	3	1	UINT16			
45627	15FA	5626				5	5 (P1)	5	1	UINT16			
45628	15FB	5627				7	7 (P1)	7	1	UINT16			
45629	15FC	5628				9	9 (P1)	9	1	UINT16			
45630	15FD	5629				11	11 (P1)	11	1	UINT16			
45631	15FE	5630				13	13 (P1)	13	1	UINT16			
45632	15FF	5631				15	15 (P1)	15	1	UINT16			
45633	1600	5632				17	17 (P1)	17	1	UINT16			
45634	1601	5633				19	19 (P1)	19	1	UINT16			
45635	1602	5634				21	21 (P1)	21	1	UINT16			
45636	1603	5635				23	23 (P1)	23	1	UINT16			
45637	1604	5636				25	25 (P1)	25	1	UINT16			
45638	1605	5637				27	27 (P1)	27	1	UINT16			
45639	1606	5638				29	29 (P1)	29	1	UINT16			
45640	1607	5639				31	31 (P1)	31	1	UINT16			
45641	1608	5640				33	33 (P1)	33	1	UINT16			
45642	1609	5641				35	35 (P1)	35	1	UINT16			
45643	160A	5642				37	37 (P1)	37	1	UINT16			
45644	160B	5643				39	39 (P1)	39	1	UINT16			
45645	160C	5644				41	41 (P1)	41	1	UINT16			
45666	1621	5665				2	2 (P1)	2	1	UINT16			
45667	1622	5666				4	4 (P1)	4	1	UINT16			
45668	1623	5667				6	6 (P1)	6	1	UINT16			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
45669	1624	5668				8	8 (P1)	8	1	UINT16			
45670	1625	5669				10	10 (P1)	10	1	UINT16			
45671	1626	5670				12	12 (P1)	12	1	UINT16			
45672	1627	5671				14	14 (P1)	14	1	UINT16			
45673	1628	5672				16	16 (P1)	16	1	UINT16			
45674	1629	5673				18	18 (P1)	18	1	UINT16			
45675	162A	5674				20	20 (P1)	20	1	UINT16			
45676	162B	5675				22	22 (P1)	22	1	UINT16			
45677	162C	5676				24	24 (P1)	24	1	UINT16			
45678	162D	5677				26	26 (P1)	26	1	UINT16			
45679	162E	5678				28	28 (P1)	28	1	UINT16			
45680	162F	5679				30	30 (P1)	30	1	UINT16			
45681	1630	5680				32	32 (P1)	32	1	UINT16			
45682	1631	5681				34	34 (P1)	34	1	UINT16			
45683	1632	5682				36	36 (P1)	36	1	UINT16			
45684	1633	5683				38	38 (P1)	38	1	UINT16			
45685	1634	5684				40	40 (P1)	40	1	UINT16			
45686	1635	5685				42	42 (P1)	42	1	UINT16			
45707	164A	5706				NA	1 (P2)	43	1	UINT16			
45708	164B	5707				NA	3 (P2)	45	1	UINT16			
45709	164C	5708				NA	5 (P2)	47	1	UINT16			
45710	164D	5709				NA	7 (P2)	49	1	UINT16			
45711	164E	5710				NA	9 (P2)	51	1	UINT16			
45712	164F	5711				NA	11 (P2)	53	1	UINT16			
45713	1650	5712				NA	13 (P2)	55	1	UINT16			
45714	1651	5713				NA	15 (P2)	57	1	UINT16			
45715	1652	5714				NA	17 (P2)	59	1	UINT16			
45716	1653	5715				NA	19 (P2)	61	1	UINT16			
45717	1654	5716				NA	21 (P2)	63	1	UINT16			
45718	1655	5717				NA	23 (P2)	65	1	UINT16			
45719	1656	5718				NA	25 (P2)	67	1	UINT16			
45720	1657	5719				NA	27 (P2)	69	1	UINT16			
45721	1658	5720				NA	29 (P2)	71	1	UINT16			
45722	1659	5721				NA	31 (P2)	73	1	UINT16			
45723	165A	5722				NA	33 (P2)	75	1	UINT16			
45724	165B	5723				NA	35 (P2)	77	1	UINT16			
45725	165C	5724				NA	37 (P2)	79	1	UINT16			
45726	165D	5725				NA	39 (P2)	81	1	UINT16			
45727	165E	5726				NA	41 (P2)	83	1	UINT16			
45748	1673	5747				NA	2 (P2)	44	1	UINT16			
45749	1674	5748				NA	4 (P2)	46	1	UINT16			
45750	1675	5749				NA	6 (P2)	48	1	UINT16			
45751	1676	5750				NA	8 (P2)	50	1	UINT16			
45752	1677	5751				NA	10 (P2)	52	1	UINT16			
45753	1678	5752				NA	12 (P2)	54	1	UINT16			
45754	1679	5753				NA	14 (P2)	56	1	UINT16			
45755	167A	5754				NA	16 (P2)	58	1	UINT16			
45756	167B	5755				NA	18 (P2)	60	1	UINT16			
45757	167C	5756				NA	20 (P2)	62	1	UINT16			
45758	167D	5757				NA	22 (P2)	64	1	UINT16			
45759	167E	5758				NA	24 (P2)	66	1	UINT16			
45760	167F	5759				NA	26 (P2)	68	1	UINT16			
45761	1680	5760				NA	28 (P2)	70	1	UINT16			
45762	1681	5761				NA	30 (P2)	72	1	UINT16			
45763	1682	5762				NA	32 (P2)	74	1	UINT16			
45764	1683	5763				NA	34 (P2)	76	1	UINT16			
45765	1684	5764				NA	36 (P2)	78	1	UINT16			
45766	1685	5765				NA	38 (P2)	80	1	UINT16			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
45767	1686	5766				NA	40 (P2)	82	1	UINT16			
45768	1687	5767				NA	42 (P2)	84	1	UINT16			
45789	169C	5788		RO	CT Size	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			Amperes, 0 = unknown 0xFFFF = unsupported
45789	169C	5788				1	1 (P1)	1	1	UINT16			
45790	169D	5789				3	3 (P1)	3	1	UINT16			
45791	169E	5790				5	5 (P1)	5	1	UINT16			
45792	169F	5791				7	7 (P1)	7	1	UINT16			
45793	16A0	5792				9	9 (P1)	9	1	UINT16			
45794	16A1	5793				11	11 (P1)	11	1	UINT16			
45795	16A2	5794				13	13 (P1)	13	1	UINT16			
45796	16A3	5795				15	15 (P1)	15	1	UINT16			
45797	16A4	5796				17	17 (P1)	17	1	UINT16			
45798	16A5	5797				19	19 (P1)	19	1	UINT16			
45799	16A6	5798				21	21 (P1)	21	1	UINT16			
45800	16A7	5799				23	23 (P1)	23	1	UINT16			
45801	16A8	5800				25	25 (P1)	25	1	UINT16			
45802	16A9	5801				27	27 (P1)	27	1	UINT16			
45803	16AA	5802				29	29 (P1)	29	1	UINT16			
45804	16AB	5803				31	31 (P1)	31	1	UINT16			
45805	16AC	5804				33	33 (P1)	33	1	UINT16			
45806	16AD	5805				35	35 (P1)	35	1	UINT16			
45807	16AE	5806				37	37 (P1)	37	1	UINT16			
45808	16AF	5807				39	39 (P1)	39	1	UINT16			
45809	16B0	5808				41	41 (P1)	41	1	UINT16			
45830	16C5	5829				2	2 (P1)	2	1	UINT16			
45831	16C6	5830				4	4 (P1)	4	1	UINT16			
45832	16C7	5831				6	6 (P1)	6	1	UINT16			
45833	16C8	5832				8	8 (P1)	8	1	UINT16			
45834	16C9	5833				10	10 (P1)	10	1	UINT16			
45835	16CA	5834				12	12 (P1)	12	1	UINT16			
45836	16CB	5835				14	14 (P1)	14	1	UINT16			
45837	16CC	5836				16	16 (P1)	16	1	UINT16			
45838	16CD	5837				18	18 (P1)	18	1	UINT16			
45839	16CE	5838				20	20 (P1)	20	1	UINT16			
45840	16CF	5839				22	22 (P1)	22	1	UINT16			
45841	16D0	5840				24	24 (P1)	24	1	UINT16			
45842	16D1	5841				26	26 (P1)	26	1	UINT16			
45843	16D2	5842				28	28 (P1)	28	1	UINT16			
45844	16D3	5843				30	30 (P1)	30	1	UINT16			
45845	16D4	5844				32	32 (P1)	32	1	UINT16			
45846	16D5	5845				34	34 (P1)	34	1	UINT16			
45847	16D6	5846				36	36 (P1)	36	1	UINT16			
45848	16D7	5847				38	38 (P1)	38	1	UINT16			
45849	16D8	5848				40	40 (P1)	40	1	UINT16			
45850	16D9	5849				42	42 (P1)	42	1	UINT16			
45871	16EE	5870				NA	1 (P2)	43	1	UINT16			
45872	16EF	5871				NA	3 (P2)	45	1	UINT16			
45873	16F0	5872				NA	5 (P2)	47	1	UINT16			
45874	16F1	5873				NA	7 (P2)	49	1	UINT16			
45875	16F2	5874				NA	9 (P2)	51	1	UINT16			
45876	16F3	5875				NA	11 (P2)	53	1	UINT16			
45877	16F4	5876				NA	13 (P2)	55	1	UINT16			
45878	16F5	5877				NA	15 (P2)	57	1	UINT16			
45879	16F6	5878				NA	17 (P2)	59	1	UINT16			
45880	16F7	5879				NA	19 (P2)	61	1	UINT16			
45881	16F8	5880				NA	21 (P2)	63	1	UINT16			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
45882	16F9	5881				NA	23 (P2)	65	1	UINT16			
45883	16FA	5882				NA	25 (P2)	67	1	UINT16			
45884	16FB	5883				NA	27 (P2)	69	1	UINT16			
45885	16FC	5884				NA	29 (P2)	71	1	UINT16			
45886	16FD	5885				NA	31 (P2)	73	1	UINT16			
45887	16FE	5886				NA	33 (P2)	75	1	UINT16			
45888	16FF	5887				NA	35 (P2)	77	1	UINT16			
45889	1700	5888				NA	37 (P2)	79	1	UINT16			
45890	1701	5889				NA	39 (P2)	81	1	UINT16			
45891	1702	5890				NA	41 (P2)	83	1	UINT16			
45912	1717	5911				NA	2 (P2)	44	1	UINT16			
45913	1718	5912				NA	4 (P2)	46	1	UINT16			
45914	1719	5913				NA	6 (P2)	48	1	UINT16			
45915	171A	5914				NA	8 (P2)	50	1	UINT16			
45916	171B	5915				NA	10 (P2)	52	1	UINT16			
45917	171C	5916				NA	12 (P2)	54	1	UINT16			
45918	171D	5917				NA	14 (P2)	56	1	UINT16			
45919	171E	5918				NA	16 (P2)	58	1	UINT16			
45920	171F	5919				NA	18 (P2)	60	1	UINT16			
45921	1720	5920				NA	20 (P2)	62	1	UINT16			
45922	1721	5921				NA	22 (P2)	64	1	UINT16			
45923	1722	5922				NA	24 (P2)	66	1	UINT16			
45924	1723	5923				NA	26 (P2)	68	1	UINT16			
45925	1724	5924				NA	28 (P2)	70	1	UINT16			
45926	1725	5925				NA	30 (P2)	72	1	UINT16			
45927	1726	5926				NA	32 (P2)	74	1	UINT16			
45928	1727	5927				NA	34 (P2)	76	1	UINT16			
45929	1728	5928				NA	36 (P2)	78	1	UINT16			
45930	1729	5929				NA	38 (P2)	80	1	UINT16			
45931	172A	5930				NA	40 (P2)	82	1	UINT16			
45932	172B	5931				NA	42 (P2)	84	1	UINT16			
<b>45953</b>	<b>1740</b>	<b>5952</b>		<b>RO</b>	<b>Threshold Enable</b>	<b>Breaker Position Panel 1x42</b>	<b>Breaker Position Panel 2x42</b>	<b>Breaker Position Panel 1x84</b>	<b>164</b>	<b>UINT16 (bool x 16)</b>			
			<b>0</b>		<b>Minimum Current Threshold Enable</b>					<b>BOOLEAN</b>			<b>1=Enable</b>
			<b>1</b>		<b>Low Current Threshold Enable</b>					<b>BOOLEAN</b>			<b>1=Enable</b>
			<b>2</b>		<b>High Current Threshold Enable</b>					<b>BOOLEAN</b>			<b>1=Enable</b>
			<b>3</b>		<b>Maximum Current Threshold Enable</b>					<b>BOOLEAN</b>			<b>1=Enable</b>
			<b>4</b>		<b>Alarm Generation Enable</b>					<b>BOOLEAN</b>			<b>1=Enable</b>
			<b>5</b>		<b>Minimum Apparent Power Threshold Enable</b>					<b>BOOLEAN</b>			<b>1=Enable</b>
			<b>6</b>		<b>Maximum Apparent Power Threshold Enable</b>					<b>BOOLEAN</b>			<b>1=Enable</b>

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
			7		NA					BOOLEAN			
			8		NA					BOOLEAN			
			9		NA					BOOLEAN			
			10		NA					BOOLEAN			
			11		NA					BOOLEAN			
			12		NA					BOOLEAN			
			13		NA					BOOLEAN			
			14		NA					BOOLEAN			
			15		NA					BOOLEAN			
45953	1740	5952				1	1 (P1)	1	1	UINT16 (bool x 16)			
45954	1741	5953				3	3 (P1)	3	1	UINT16 (bool x 16)			
45955	1742	5954				5	5 (P1)	5	1	UINT16 (bool x 16)			
45956	1743	5955				7	7 (P1)	7	1	UINT16 (bool x 16)			
45957	1744	5956				9	9 (P1)	9	1	UINT16 (bool x 16)			
45958	1745	5957				11	11 (P1)	11	1	UINT16 (bool x 16)			
45959	1746	5958				13	13 (P1)	13	1	UINT16 (bool x 16)			
45960	1747	5959				15	15 (P1)	15	1	UINT16 (bool x 16)			
45961	1748	5960				17	17 (P1)	17	1	UINT16 (bool x 16)			
45962	1749	5961				19	19 (P1)	19	1	UINT16 (bool x 16)			
45963	174A	5962				21	21 (P1)	21	1	UINT16 (bool x 16)			
45964	174B	5963				23	23 (P1)	23	1	UINT16 (bool x 16)			
45965	174C	5964				25	25 (P1)	25	1	UINT16 (bool x 16)			
45966	174D	5965				27	27 (P1)	27	1	UINT16 (bool x 16)			
45967	174E	5966				29	29 (P1)	29	1	UINT16 (bool x 16)			



Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
45968	174F	5967				31	31 (P1)	31	1	UINT16 (bool x 16)			
45969	1750	5968				33	33 (P1)	33	1	UINT16 (bool x 16)			
45970	1751	5969				35	35 (P1)	35	1	UINT16 (bool x 16)			
45971	1752	5970				37	37 (P1)	37	1	UINT16 (bool x 16)			
45972	1753	5971				39	39 (P1)	39	1	UINT16 (bool x 16)			
45973	1754	5972				41	41 (P1)	41	1	UINT16 (bool x 16)			
45994	1769	5993				2	2 (P1)	2	1	UINT16 (bool x 16)			
45995	176A	5994				4	4 (P1)	4	1	UINT16 (bool x 16)			
45996	176B	5995				6	6 (P1)	6	1	UINT16 (bool x 16)			
45997	176C	5996				8	8 (P1)	8	1	UINT16 (bool x 16)			
45998	176D	5997				10	10 (P1)	10	1	UINT16 (bool x 16)			
45999	176E	5998				12	12 (P1)	12	1	UINT16 (bool x 16)			
46000	176F	5999				14	14 (P1)	14	1	UINT16 (bool x 16)			
46001	1770	6000				16	16 (P1)	16	1	UINT16 (bool x 16)			
46002	1771	6001				18	18 (P1)	18	1	UINT16 (bool x 16)			
46003	1772	6002				20	20 (P1)	20	1	UINT16 (bool x 16)			
46004	1773	6003				22	22 (P1)	22	1	UINT16 (bool x 16)			
46005	1774	6004				24	24 (P1)	24	1	UINT16 (bool x 16)			
46006	1775	6005				26	26 (P1)	26	1	UINT16 (bool x 16)			
46007	1776	6006				28	28 (P1)	28	1	UINT16 (bool x 16)			
46008	1777	6007				30	30 (P1)	30	1	UINT16 (bool x 16)			
46009	1778	6008				32	32 (P1)	32	1	UINT16 (bool x 16)			
46010	1779	6009				34	34 (P1)	34	1	UINT16 (bool x 16)			
46011	177A	6010				36	36 (P1)	36	1	UINT16 (bool x 16)			
46012	177B	6011				38	38 (P1)	38	1	UINT16 (bool x 16)			
46013	177C	6012				40	40 (P1)	40	1	UINT16 (bool x 16)			
46014	177D	6013				42	42 (P1)	42	1	UINT16 (bool x 16)			
46035	1792	6034				NA	1 (P2)	43	1	UINT16 (bool x 16)			
46036	1793	6035				NA	3 (P2)	45	1	UINT16 (bool x 16)			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46037	1794	6036				NA	5 (P2)	47	1	UINT16 (bool x 16)			
46038	1795	6037				NA	7 (P2)	49	1	UINT16 (bool x 16)			
46039	1796	6038				NA	9 (P2)	51	1	UINT16 (bool x 16)			
46040	1797	6039				NA	11 (P2)	53	1	UINT16 (bool x 16)			
46041	1798	6040				NA	13 (P2)	55	1	UINT16 (bool x 16)			
46042	1799	6041				NA	15 (P2)	57	1	UINT16 (bool x 16)			
46043	179A	6042				NA	17 (P2)	59	1	UINT16 (bool x 16)			
46044	179B	6043				NA	19 (P2)	61	1	UINT16 (bool x 16)			
46045	179C	6044				NA	21 (P2)	63	1	UINT16 (bool x 16)			
46046	179D	6045				NA	23 (P2)	65	1	UINT16 (bool x 16)			
46047	179E	6046				NA	25 (P2)	67	1	UINT16 (bool x 16)			
46048	179F	6047				NA	27 (P2)	69	1	UINT16 (bool x 16)			
46049	17A0	6048				NA	29 (P2)	71	1	UINT16 (bool x 16)			
46050	17A1	6049				NA	31 (P2)	73	1	UINT16 (bool x 16)			
46051	17A2	6050				NA	33 (P2)	75	1	UINT16 (bool x 16)			
46052	17A3	6051				NA	35 (P2)	77	1	UINT16 (bool x 16)			
46053	17A4	6052				NA	37 (P2)	79	1	UINT16 (bool x 16)			
46054	17A5	6053				NA	39 (P2)	81	1	UINT16 (bool x 16)			
46055	17A6	6054				NA	41 (P2)	83	1	UINT16 (bool x 16)			
46076	17BB	6075				NA	2 (P2)	44	1	UINT16 (bool x 16)			
46077	17BC	6076				NA	4 (P2)	46	1	UINT16 (bool x 16)			
46078	17BD	6077				NA	6 (P2)	48	1	UINT16 (bool x 16)			
46079	17BE	6078				NA	8 (P2)	50	1	UINT16 (bool x 16)			
46080	17BF	6079				NA	10 (P2)	52	1	UINT16 (bool x 16)			
46081	17C0	6080				NA	12 (P2)	54	1	UINT16 (bool x 16)			
46082	17C1	6081				NA	14 (P2)	56	1	UINT16 (bool x 16)			
46083	17C2	6082				NA	16 (P2)	58	1	UINT16 (bool x 16)			
46084	17C3	6083				NA	18 (P2)	60	1	UINT16 (bool x 16)			
46085	17C4	6084				NA	20 (P2)	62	1	UINT16 (bool x 16)			

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46086	17C5	6085				NA	22 (P2)	64	1	UINT16 (bool x 16)			
46087	17C6	6086				NA	24 (P2)	66	1	UINT16 (bool x 16)			
46088	17C7	6087				NA	26 (P2)	68	1	UINT16 (bool x 16)			
46089	17C8	6088				NA	28 (P2)	70	1	UINT16 (bool x 16)			
46090	17C9	6089				NA	30 (P2)	72	1	UINT16 (bool x 16)			
46091	17CA	6090				NA	32 (P2)	74	1	UINT16 (bool x 16)			
46092	17CB	6091				NA	34 (P2)	76	1	UINT16 (bool x 16)			
46093	17CC	6092				NA	36 (P2)	78	1	UINT16 (bool x 16)			
46094	17CD	6093				NA	38 (P2)	80	1	UINT16 (bool x 16)			
46095	17CE	6094				NA	40 (P2)	82	1	UINT16 (bool x 16)			
46096	17CF	6095				NA	42 (P2)	84	1	UINT16 (bool x 16)			
46117	17E4	6116		RO	Minimum Current Threshold	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			
46117	17E4	6116				1	1 (P1)	1	1	UINT16			%
46118	17E5	6117				3	3 (P1)	3	1	UINT16			%
46119	17E6	6118				5	5 (P1)	5	1	UINT16			%
46120	17E7	6119				7	7 (P1)	7	1	UINT16			%
46121	17E8	6120				9	9 (P1)	9	1	UINT16			%
46122	17E9	6121				11	11 (P1)	11	1	UINT16			%
46123	17EA	6122				13	13 (P1)	13	1	UINT16			%
46124	17EB	6123				15	15 (P1)	15	1	UINT16			%
46125	17EC	6124				17	17 (P1)	17	1	UINT16			%
46126	17ED	6125				19	19 (P1)	19	1	UINT16			%
46127	17EE	6126				21	21 (P1)	21	1	UINT16			%
46128	17EF	6127				23	23 (P1)	23	1	UINT16			%
46129	17F0	6128				25	25 (P1)	25	1	UINT16			%
46130	17F1	6129				27	27 (P1)	27	1	UINT16			%
46131	17F2	6130				29	29 (P1)	29	1	UINT16			%
46132	17F3	6131				31	31 (P1)	31	1	UINT16			%
46133	17F4	6132				33	33 (P1)	33	1	UINT16			%
46134	17F5	6133				35	35 (P1)	35	1	UINT16			%
46135	17F6	6134				37	37 (P1)	37	1	UINT16			%
46136	17F7	6135				39	39 (P1)	39	1	UINT16			%
46137	17F8	6136				41	41 (P1)	41	1	UINT16			%
46158	180D	6157				2	2 (P1)	2	1	UINT16			%
46159	180E	6158				4	4 (P1)	4	1	UINT16			%
46160	180F	6159				6	6 (P1)	6	1	UINT16			%
46161	1810	6160				8	8 (P1)	8	1	UINT16			%
46162	1811	6161				10	10 (P1)	10	1	UINT16			%
46163	1812	6162				12	12 (P1)	12	1	UINT16			%
46164	1813	6163				14	14 (P1)	14	1	UINT16			%
46165	1814	6164				16	16 (P1)	16	1	UINT16			%
46166	1815	6165				18	18 (P1)	18	1	UINT16			%
46167	1816	6166				20	20 (P1)	20	1	UINT16			%
46168	1817	6167				22	22 (P1)	22	1	UINT16			%
46169	1818	6168				24	24 (P1)	24	1	UINT16			%

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46170	1819	6169				26	26 (P1)	26	1	UINT16			%
46171	181A	6170				28	28 (P1)	28	1	UINT16			%
46172	181B	6171				30	30 (P1)	30	1	UINT16			%
46173	181C	6172				32	32 (P1)	32	1	UINT16			%
46174	181D	6173				34	34 (P1)	34	1	UINT16			%
46175	181E	6174				36	36 (P1)	36	1	UINT16			%
46176	181F	6175				38	38 (P1)	38	1	UINT16			%
46177	1820	6176				40	40 (P1)	40	1	UINT16			%
46178	1821	6177				42	42 (P1)	42	1	UINT16			%
46199	1836	6198				NA	1 (P2)	43	1	UINT16			%
46200	1837	6199				NA	3 (P2)	45	1	UINT16			%
46201	1838	6200				NA	5 (P2)	47	1	UINT16			%
46202	1839	6201				NA	7 (P2)	49	1	UINT16			%
46203	183A	6202				NA	9 (P2)	51	1	UINT16			%
46204	183B	6203				NA	11 (P2)	53	1	UINT16			%
46205	183C	6204				NA	13 (P2)	55	1	UINT16			%
46206	183D	6205				NA	15 (P2)	57	1	UINT16			%
46207	183E	6206				NA	17 (P2)	59	1	UINT16			%
46208	183F	6207				NA	19 (P2)	61	1	UINT16			%
46209	1840	6208				NA	21 (P2)	63	1	UINT16			%
46210	1841	6209				NA	23 (P2)	65	1	UINT16			%
46211	1842	6210				NA	25 (P2)	67	1	UINT16			%
46212	1843	6211				NA	27 (P2)	69	1	UINT16			%
46213	1844	6212				NA	29 (P2)	71	1	UINT16			%
46214	1845	6213				NA	31 (P2)	73	1	UINT16			%
46215	1846	6214				NA	33 (P2)	75	1	UINT16			%
46216	1847	6215				NA	35 (P2)	77	1	UINT16			%
46217	1848	6216				NA	37 (P2)	79	1	UINT16			%
46218	1849	6217				NA	39 (P2)	81	1	UINT16			%
46219	184A	6218				NA	41 (P2)	83	1	UINT16			%
46240	185F	6239				NA	2 (P2)	44	1	UINT16			%
46241	1860	6240				NA	4 (P2)	46	1	UINT16			%
46242	1861	6241				NA	6 (P2)	48	1	UINT16			%
46243	1862	6242				NA	8 (P2)	50	1	UINT16			%
46244	1863	6243				NA	10 (P2)	52	1	UINT16			%
46245	1864	6244				NA	12 (P2)	54	1	UINT16			%
46246	1865	6245				NA	14 (P2)	56	1	UINT16			%
46247	1866	6246				NA	16 (P2)	58	1	UINT16			%
46248	1867	6247				NA	18 (P2)	60	1	UINT16			%
46249	1868	6248				NA	20 (P2)	62	1	UINT16			%
46250	1869	6249				NA	22 (P2)	64	1	UINT16			%
46251	186A	6250				NA	24 (P2)	66	1	UINT16			%
46252	186B	6251				NA	26 (P2)	68	1	UINT16			%
46253	186C	6252				NA	28 (P2)	70	1	UINT16			%
46254	186D	6253				NA	30 (P2)	72	1	UINT16			%
46255	186E	6254				NA	32 (P2)	74	1	UINT16			%
46256	186F	6255				NA	34 (P2)	76	1	UINT16			%
46257	1870	6256				NA	36 (P2)	78	1	UINT16			%
46258	1871	6257				NA	38 (P2)	80	1	UINT16			%
46259	1872	6258				NA	40 (P2)	82	1	UINT16			%
46260	1873	6259				NA	42 (P2)	84	1	UINT16			%
46281	1888	6280		RO	Low Current Threshold	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			
46281	1888	6280				1	1 (P1)	1	1	UINT16			%
46282	1889	6281				3	3 (P1)	3	1	UINT16			%
46283	188A	6282				5	5 (P1)	5	1	UINT16			%
46284	188B	6283				7	7 (P1)	7	1	UINT16			%

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46285	188C	6284				9	9 (P1)	9	1	UINT16			%
46286	188D	6285				11	11 (P1)	11	1	UINT16			%
46287	188E	6286				13	13 (P1)	13	1	UINT16			%
46288	188F	6287				15	15 (P1)	15	1	UINT16			%
46289	1890	6288				17	17 (P1)	17	1	UINT16			%
46290	1891	6289				19	19 (P1)	19	1	UINT16			%
46291	1892	6290				21	21 (P1)	21	1	UINT16			%
46292	1893	6291				23	23 (P1)	23	1	UINT16			%
46293	1894	6292				25	25 (P1)	25	1	UINT16			%
46294	1895	6293				27	27 (P1)	27	1	UINT16			%
46295	1896	6294				29	29 (P1)	29	1	UINT16			%
46296	1897	6295				31	31 (P1)	31	1	UINT16			%
46297	1898	6296				33	33 (P1)	33	1	UINT16			%
46298	1899	6297				35	35 (P1)	35	1	UINT16			%
46299	189A	6298				37	37 (P1)	37	1	UINT16			%
46300	189B	6299				39	39 (P1)	39	1	UINT16			%
46301	189C	6300				41	41 (P1)	41	1	UINT16			%
46322	18B1	6321				2	2 (P1)	2	1	UINT16			%
46323	18B2	6322				4	4 (P1)	4	1	UINT16			%
46324	18B3	6323				6	6 (P1)	6	1	UINT16			%
46325	18B4	6324				8	8 (P1)	8	1	UINT16			%
46326	18B5	6325				10	10 (P1)	10	1	UINT16			%
46327	18B6	6326				12	12 (P1)	12	1	UINT16			%
46328	18B7	6327				14	14 (P1)	14	1	UINT16			%
46329	18B8	6328				16	16 (P1)	16	1	UINT16			%
46330	18B9	6329				18	18 (P1)	18	1	UINT16			%
46331	18BA	6330				20	20 (P1)	20	1	UINT16			%
46332	18BB	6331				22	22 (P1)	22	1	UINT16			%
46333	18BC	6332				24	24 (P1)	24	1	UINT16			%
46334	18BD	6333				26	26 (P1)	26	1	UINT16			%
46335	18BE	6334				28	28 (P1)	28	1	UINT16			%
46336	18BF	6335				30	30 (P1)	30	1	UINT16			%
46337	18C0	6336				32	32 (P1)	32	1	UINT16			%
46338	18C1	6337				34	34 (P1)	34	1	UINT16			%
46339	18C2	6338				36	36 (P1)	36	1	UINT16			%
46340	18C3	6339				38	38 (P1)	38	1	UINT16			%
46341	18C4	6340				40	40 (P1)	40	1	UINT16			%
46342	18C5	6341				42	42 (P1)	42	1	UINT16			%
46363	18DA	6362				NA	1 (P2)	43	1	UINT16			%
46364	18DB	6363				NA	3 (P2)	45	1	UINT16			%
46365	18DC	6364				NA	5 (P2)	47	1	UINT16			%
46366	18DD	6365				NA	7 (P2)	49	1	UINT16			%
46367	18DE	6366				NA	9 (P2)	51	1	UINT16			%
46368	18DF	6367				NA	11 (P2)	53	1	UINT16			%
46369	18E0	6368				NA	13 (P2)	55	1	UINT16			%
46370	18E1	6369				NA	15 (P2)	57	1	UINT16			%
46371	18E2	6370				NA	17 (P2)	59	1	UINT16			%
46372	18E3	6371				NA	19 (P2)	61	1	UINT16			%
46373	18E4	6372				NA	21 (P2)	63	1	UINT16			%
46374	18E5	6373				NA	23 (P2)	65	1	UINT16			%
46375	18E6	6374				NA	25 (P2)	67	1	UINT16			%
46376	18E7	6375				NA	27 (P2)	69	1	UINT16			%
46377	18E8	6376				NA	29 (P2)	71	1	UINT16			%
46378	18E9	6377				NA	31 (P2)	73	1	UINT16			%
46379	18EA	6378				NA	33 (P2)	75	1	UINT16			%
46380	18EB	6379				NA	35 (P2)	77	1	UINT16			%
46381	18EC	6380				NA	37 (P2)	79	1	UINT16			%
46382	18ED	6381				NA	39 (P2)	81	1	UINT16			%

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46383	18EE	6382				NA	41 (P2)	83	1	UINT16			%
46404	1903	6403				NA	2 (P2)	44	1	UINT16			%
46405	1904	6404				NA	4 (P2)	46	1	UINT16			%
46406	1905	6405				NA	6 (P2)	48	1	UINT16			%
46407	1906	6406				NA	8 (P2)	50	1	UINT16			%
46408	1907	6407				NA	10 (P2)	52	1	UINT16			%
46409	1908	6408				NA	12 (P2)	54	1	UINT16			%
46410	1909	6409				NA	14 (P2)	56	1	UINT16			%
46411	190A	6410				NA	16 (P2)	58	1	UINT16			%
46412	190B	6411				NA	18 (P2)	60	1	UINT16			%
46413	190C	6412				NA	20 (P2)	62	1	UINT16			%
46414	190D	6413				NA	22 (P2)	64	1	UINT16			%
46415	190E	6414				NA	24 (P2)	66	1	UINT16			%
46416	190F	6415				NA	26 (P2)	68	1	UINT16			%
46417	1910	6416				NA	28 (P2)	70	1	UINT16			%
46418	1911	6417				NA	30 (P2)	72	1	UINT16			%
46419	1912	6418				NA	32 (P2)	74	1	UINT16			%
46420	1913	6419				NA	34 (P2)	76	1	UINT16			%
46421	1914	6420				NA	36 (P2)	78	1	UINT16			%
46422	1915	6421				NA	38 (P2)	80	1	UINT16			%
46423	1916	6422				NA	40 (P2)	82	1	UINT16			%
46424	1917	6423				NA	42 (P2)	84	1	UINT16			%
46445	192C	6444		RO	High Current Threshold	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			
46445	192C	6444				1	1 (P1)	1	1	UINT16		100	kW, 0xFFFF = unsupported
46446	192D	6445				3	3 (P1)	3	1	UINT16		100	kW, 0xFFFF = unsupported
46447	192E	6446				5	5 (P1)	5	1	UINT16		100	kW, 0xFFFF = unsupported
46448	192F	6447				7	7 (P1)	7	1	UINT16		100	kW, 0xFFFF = unsupported
46449	1930	6448				9	9 (P1)	9	1	UINT16		100	kW, 0xFFFF = unsupported
46450	1931	6449				11	11 (P1)	11	1	UINT16		100	kW, 0xFFFF = unsupported
46451	1932	6450				13	13 (P1)	13	1	UINT16		100	kW, 0xFFFF = unsupported
46452	1933	6451				15	15 (P1)	15	1	UINT16		100	kW, 0xFFFF = unsupported
46453	1934	6452				17	17 (P1)	17	1	UINT16		100	kW, 0xFFFF = unsupported
46454	1935	6453				19	19 (P1)	19	1	UINT16		100	kW, 0xFFFF = unsupported
46455	1936	6454				21	21 (P1)	21	1	UINT16		100	kW, 0xFFFF = unsupported
46456	1937	6455				23	23 (P1)	23	1	UINT16		100	kW, 0xFFFF = unsupported
46457	1938	6456				25	25 (P1)	25	1	UINT16		100	kW, 0xFFFF = unsupported
46458	1939	6457				27	27 (P1)	27	1	UINT16		100	kW, 0xFFFF = unsupported
46459	193A	6458				29	29 (P1)	29	1	UINT16		100	kW, 0xFFFF = unsupported
46460	193B	6459				31	31 (P1)	31	1	UINT16		100	kW, 0xFFFF = unsupported
46461	193C	6460				33	33 (P1)	33	1	UINT16		100	kW, 0xFFFF = unsupported
46462	193D	6461				35	35 (P1)	35	1	UINT16		100	kW, 0xFFFF = unsupported
46463	193E	6462				37	37 (P1)	37	1	UINT16		100	kW, 0xFFFF = unsupported
46464	193F	6463				39	39 (P1)	39	1	UINT16		100	kW, 0xFFFF = unsupported
46465	1940	6464				41	41 (P1)	41	1	UINT16		100	kW, 0xFFFF = unsupported
46486	1955	6485				2	2 (P1)	2	1	UINT16		100	kW, 0xFFFF = unsupported
46487	1956	6486				4	4 (P1)	4	1	UINT16		100	kW, 0xFFFF = unsupported
46488	1957	6487				6	6 (P1)	6	1	UINT16		100	kW, 0xFFFF = unsupported
46489	1958	6488				8	8 (P1)	8	1	UINT16		100	kW, 0xFFFF = unsupported
46490	1959	6489				10	10 (P1)	10	1	UINT16		100	kW, 0xFFFF = unsupported
46491	195A	6490				12	12 (P1)	12	1	UINT16		100	kW, 0xFFFF = unsupported
46492	195B	6491				14	14 (P1)	14	1	UINT16		100	kW, 0xFFFF = unsupported
46493	195C	6492				16	16 (P1)	16	1	UINT16		100	kW, 0xFFFF = unsupported
46494	195D	6493				18	18 (P1)	18	1	UINT16		100	kW, 0xFFFF = unsupported
46495	195E	6494				20	20 (P1)	20	1	UINT16		100	kW, 0xFFFF = unsupported
46496	195F	6495				22	22 (P1)	22	1	UINT16		100	kW, 0xFFFF = unsupported
46497	1960	6496				24	24 (P1)	24	1	UINT16		100	kW, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46498	1961	6497				26	26 (P1)	26	1	UINT16		100	kW, 0xFFFF = unsupported
46499	1962	6498				28	28 (P1)	28	1	UINT16		100	kW, 0xFFFF = unsupported
46500	1963	6499				30	30 (P1)	30	1	UINT16		100	kW, 0xFFFF = unsupported
46501	1964	6500				32	32 (P1)	32	1	UINT16		100	kW, 0xFFFF = unsupported
46502	1965	6501				34	34 (P1)	34	1	UINT16		100	kW, 0xFFFF = unsupported
46503	1966	6502				36	36 (P1)	36	1	UINT16		100	kW, 0xFFFF = unsupported
46504	1967	6503				38	38 (P1)	38	1	UINT16		100	kW, 0xFFFF = unsupported
46505	1968	6504				40	40 (P1)	40	1	UINT16		100	kW, 0xFFFF = unsupported
46506	1969	6505				42	42 (P1)	42	1	UINT16		100	kW, 0xFFFF = unsupported
46527	197E	6526				NA	1 (P2)	43	1	UINT16		100	kW, 0xFFFF = unsupported
46528	197F	6527				NA	3 (P2)	45	1	UINT16		100	kW, 0xFFFF = unsupported
46529	1980	6528				NA	5 (P2)	47	1	UINT16		100	kW, 0xFFFF = unsupported
46530	1981	6529				NA	7 (P2)	49	1	UINT16		100	kW, 0xFFFF = unsupported
46531	1982	6530				NA	9 (P2)	51	1	UINT16		100	kW, 0xFFFF = unsupported
46532	1983	6531				NA	11 (P2)	53	1	UINT16		100	kW, 0xFFFF = unsupported
46533	1984	6532				NA	13 (P2)	55	1	UINT16		100	kW, 0xFFFF = unsupported
46534	1985	6533				NA	15 (P2)	57	1	UINT16		100	kW, 0xFFFF = unsupported
46535	1986	6534				NA	17 (P2)	59	1	UINT16		100	kW, 0xFFFF = unsupported
46536	1987	6535				NA	19 (P2)	61	1	UINT16		100	kW, 0xFFFF = unsupported
46537	1988	6536				NA	21 (P2)	63	1	UINT16		100	kW, 0xFFFF = unsupported
46538	1989	6537				NA	23 (P2)	65	1	UINT16		100	kW, 0xFFFF = unsupported
46539	198A	6538				NA	25 (P2)	67	1	UINT16		100	kW, 0xFFFF = unsupported
46540	198B	6539				NA	27 (P2)	69	1	UINT16		100	kW, 0xFFFF = unsupported
46541	198C	6540				NA	29 (P2)	71	1	UINT16		100	kW, 0xFFFF = unsupported
46542	198D	6541				NA	31 (P2)	73	1	UINT16		100	kW, 0xFFFF = unsupported
46543	198E	6542				NA	33 (P2)	75	1	UINT16		100	kW, 0xFFFF = unsupported
46544	198F	6543				NA	35 (P2)	77	1	UINT16		100	kW, 0xFFFF = unsupported
46545	1990	6544				NA	37 (P2)	79	1	UINT16		100	kW, 0xFFFF = unsupported
46546	1991	6545				NA	39 (P2)	81	1	UINT16		100	kW, 0xFFFF = unsupported
46547	1992	6546				NA	41 (P2)	83	1	UINT16		100	kW, 0xFFFF = unsupported
46568	19A7	6567				NA	2 (P2)	44	1	UINT16		100	kW, 0xFFFF = unsupported
46569	19A8	6568				NA	4 (P2)	46	1	UINT16		100	kW, 0xFFFF = unsupported
46570	19A9	6569				NA	6 (P2)	48	1	UINT16		100	kW, 0xFFFF = unsupported
46571	19AA	6570				NA	8 (P2)	50	1	UINT16		100	kW, 0xFFFF = unsupported
46572	19AB	6571				NA	10 (P2)	52	1	UINT16		100	kW, 0xFFFF = unsupported
46573	19AC	6572				NA	12 (P2)	54	1	UINT16		100	kW, 0xFFFF = unsupported
46574	19AD	6573				NA	14 (P2)	56	1	UINT16		100	kW, 0xFFFF = unsupported
46575	19AE	6574				NA	16 (P2)	58	1	UINT16		100	kW, 0xFFFF = unsupported
46576	19AF	6575				NA	18 (P2)	60	1	UINT16		100	kW, 0xFFFF = unsupported
46577	19B0	6576				NA	20 (P2)	62	1	UINT16		100	kW, 0xFFFF = unsupported
46578	19B1	6577				NA	22 (P2)	64	1	UINT16		100	kW, 0xFFFF = unsupported
46579	19B2	6578				NA	24 (P2)	66	1	UINT16		100	kW, 0xFFFF = unsupported
46580	19B3	6579				NA	26 (P2)	68	1	UINT16		100	kW, 0xFFFF = unsupported
46581	19B4	6580				NA	28 (P2)	70	1	UINT16		100	kW, 0xFFFF = unsupported
46582	19B5	6581				NA	30 (P2)	72	1	UINT16		100	kW, 0xFFFF = unsupported
46583	19B6	6582				NA	32 (P2)	74	1	UINT16		100	kW, 0xFFFF = unsupported
46584	19B7	6583				NA	34 (P2)	76	1	UINT16		100	kW, 0xFFFF = unsupported
46585	19B8	6584				NA	36 (P2)	78	1	UINT16		100	kW, 0xFFFF = unsupported
46586	19B9	6585				NA	38 (P2)	80	1	UINT16		100	kW, 0xFFFF = unsupported
46587	19BA	6586				NA	40 (P2)	82	1	UINT16		100	kW, 0xFFFF = unsupported
46588	19BB	6587				NA	42 (P2)	84	1	UINT16		100	kW, 0xFFFF = unsupported
46609	19D0	6608		RO	Maximum Current Threshold	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			
46609	19D0	6608				1	1 (P1)	1	1	UINT16		100	kW, 0xFFFF = unsupported
46610	19D1	6609				3	3 (P1)	3	1	UINT16		100	kW, 0xFFFF = unsupported
46611	19D2	6610				5	5 (P1)	5	1	UINT16		100	kW, 0xFFFF = unsupported
46612	19D3	6611				7	7 (P1)	7	1	UINT16		100	kW, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46613	19D4	6612				9	9 (P1)	9	1	UINT16		100	kW, 0xFFFF = unsupported
46614	19D5	6613				11	11 (P1)	11	1	UINT16		100	kW, 0xFFFF = unsupported
46615	19D6	6614				13	13 (P1)	13	1	UINT16		100	kW, 0xFFFF = unsupported
46616	19D7	6615				15	15 (P1)	15	1	UINT16		100	kW, 0xFFFF = unsupported
46617	19D8	6616				17	17 (P1)	17	1	UINT16		100	kW, 0xFFFF = unsupported
46618	19D9	6617				19	19 (P1)	19	1	UINT16		100	kW, 0xFFFF = unsupported
46619	19DA	6618				21	21 (P1)	21	1	UINT16		100	kW, 0xFFFF = unsupported
46620	19DB	6619				23	23 (P1)	23	1	UINT16		100	kW, 0xFFFF = unsupported
46621	19DC	6620				25	25 (P1)	25	1	UINT16		100	kW, 0xFFFF = unsupported
46622	19DD	6621				27	27 (P1)	27	1	UINT16		100	kW, 0xFFFF = unsupported
46623	19DE	6622				29	29 (P1)	29	1	UINT16		100	kW, 0xFFFF = unsupported
46624	19DF	6623				31	31 (P1)	31	1	UINT16		100	kW, 0xFFFF = unsupported
46625	19E0	6624				33	33 (P1)	33	1	UINT16		100	kW, 0xFFFF = unsupported
46626	19E1	6625				35	35 (P1)	35	1	UINT16		100	kW, 0xFFFF = unsupported
46627	19E2	6626				37	37 (P1)	37	1	UINT16		100	kW, 0xFFFF = unsupported
46628	19E3	6627				39	39 (P1)	39	1	UINT16		100	kW, 0xFFFF = unsupported
46629	19E4	6628				41	41 (P1)	41	1	UINT16		100	kW, 0xFFFF = unsupported
46650	19F9	6649				2	2 (P1)	2	1	UINT16		100	kW, 0xFFFF = unsupported
46651	19FA	6650				4	4 (P1)	4	1	UINT16		100	kW, 0xFFFF = unsupported
46652	19FB	6651				6	6 (P1)	6	1	UINT16		100	kW, 0xFFFF = unsupported
46653	19FC	6652				8	8 (P1)	8	1	UINT16		100	kW, 0xFFFF = unsupported
46654	19FD	6653				10	10 (P1)	10	1	UINT16		100	kW, 0xFFFF = unsupported
46655	19FE	6654				12	12 (P1)	12	1	UINT16		100	kW, 0xFFFF = unsupported
46656	19FF	6655				14	14 (P1)	14	1	UINT16		100	kW, 0xFFFF = unsupported
46657	1A00	6656				16	16 (P1)	16	1	UINT16		100	kW, 0xFFFF = unsupported
46658	1A01	6657				18	18 (P1)	18	1	UINT16		100	kW, 0xFFFF = unsupported
46659	1A02	6658				20	20 (P1)	20	1	UINT16		100	kW, 0xFFFF = unsupported
46660	1A03	6659				22	22 (P1)	22	1	UINT16		100	kW, 0xFFFF = unsupported
46661	1A04	6660				24	24 (P1)	24	1	UINT16		100	kW, 0xFFFF = unsupported
46662	1A05	6661				26	26 (P1)	26	1	UINT16		100	kW, 0xFFFF = unsupported
46663	1A06	6662				28	28 (P1)	28	1	UINT16		100	kW, 0xFFFF = unsupported
46664	1A07	6663				30	30 (P1)	30	1	UINT16		100	kW, 0xFFFF = unsupported
46665	1A08	6664				32	32 (P1)	32	1	UINT16		100	kW, 0xFFFF = unsupported
46666	1A09	6665				34	34 (P1)	34	1	UINT16		100	kW, 0xFFFF = unsupported
46667	1A0A	6666				36	36 (P1)	36	1	UINT16		100	kW, 0xFFFF = unsupported
46668	1A0B	6667				38	38 (P1)	38	1	UINT16		100	kW, 0xFFFF = unsupported
46669	1A0C	6668				40	40 (P1)	40	1	UINT16		100	kW, 0xFFFF = unsupported
46670	1A0D	6669				42	42 (P1)	42	1	UINT16		100	kW, 0xFFFF = unsupported
46691	1A22	6690				NA	1 (P2)	43	1	UINT16		100	kW, 0xFFFF = unsupported
46692	1A23	6691				NA	3 (P2)	45	1	UINT16		100	kW, 0xFFFF = unsupported
46693	1A24	6692				NA	5 (P2)	47	1	UINT16		100	kW, 0xFFFF = unsupported
46694	1A25	6693				NA	7 (P2)	49	1	UINT16		100	kW, 0xFFFF = unsupported
46695	1A26	6694				NA	9 (P2)	51	1	UINT16		100	kW, 0xFFFF = unsupported
46696	1A27	6695				NA	11 (P2)	53	1	UINT16		100	kW, 0xFFFF = unsupported
46697	1A28	6696				NA	13 (P2)	55	1	UINT16		100	kW, 0xFFFF = unsupported
46698	1A29	6697				NA	15 (P2)	57	1	UINT16		100	kW, 0xFFFF = unsupported
46699	1A2A	6698				NA	17 (P2)	59	1	UINT16		100	kW, 0xFFFF = unsupported
46700	1A2B	6699				NA	19 (P2)	61	1	UINT16		100	kW, 0xFFFF = unsupported
46701	1A2C	6700				NA	21 (P2)	63	1	UINT16		100	kW, 0xFFFF = unsupported
46702	1A2D	6701				NA	23 (P2)	65	1	UINT16		100	kW, 0xFFFF = unsupported
46703	1A2E	6702				NA	25 (P2)	67	1	UINT16		100	kW, 0xFFFF = unsupported
46704	1A2F	6703				NA	27 (P2)	69	1	UINT16		100	kW, 0xFFFF = unsupported
46705	1A30	6704				NA	29 (P2)	71	1	UINT16		100	kW, 0xFFFF = unsupported
46706	1A31	6705				NA	31 (P2)	73	1	UINT16		100	kW, 0xFFFF = unsupported
46707	1A32	6706				NA	33 (P2)	75	1	UINT16		100	kW, 0xFFFF = unsupported
46708	1A33	6707				NA	35 (P2)	77	1	UINT16		100	kW, 0xFFFF = unsupported
46709	1A34	6708				NA	37 (P2)	79	1	UINT16		100	kW, 0xFFFF = unsupported
46710	1A35	6709				NA	39 (P2)	81	1	UINT16		100	kW, 0xFFFF = unsupported



Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46711	1A36	6710				NA	41 (P2)	83	1	UINT16		100	kW, 0xFFFF = unsupported
46732	1A4B	6731				NA	2 (P2)	44	1	UINT16		100	kW, 0xFFFF = unsupported
46733	1A4C	6732				NA	4 (P2)	46	1	UINT16		100	kW, 0xFFFF = unsupported
46734	1A4D	6733				NA	6 (P2)	48	1	UINT16		100	kW, 0xFFFF = unsupported
46735	1A4E	6734				NA	8 (P2)	50	1	UINT16		100	kW, 0xFFFF = unsupported
46736	1A4F	6735				NA	10 (P2)	52	1	UINT16		100	kW, 0xFFFF = unsupported
46737	1A50	6736				NA	12 (P2)	54	1	UINT16		100	kW, 0xFFFF = unsupported
46738	1A51	6737				NA	14 (P2)	56	1	UINT16		100	kW, 0xFFFF = unsupported
46739	1A52	6738				NA	16 (P2)	58	1	UINT16		100	kW, 0xFFFF = unsupported
46740	1A53	6739				NA	18 (P2)	60	1	UINT16		100	kW, 0xFFFF = unsupported
46741	1A54	6740				NA	20 (P2)	62	1	UINT16		100	kW, 0xFFFF = unsupported
46742	1A55	6741				NA	22 (P2)	64	1	UINT16		100	kW, 0xFFFF = unsupported
46743	1A56	6742				NA	24 (P2)	66	1	UINT16		100	kW, 0xFFFF = unsupported
46744	1A57	6743				NA	26 (P2)	68	1	UINT16		100	kW, 0xFFFF = unsupported
46745	1A58	6744				NA	28 (P2)	70	1	UINT16		100	kW, 0xFFFF = unsupported
46746	1A59	6745				NA	30 (P2)	72	1	UINT16		100	kW, 0xFFFF = unsupported
46747	1A5A	6746				NA	32 (P2)	74	1	UINT16		100	kW, 0xFFFF = unsupported
46748	1A5B	6747				NA	34 (P2)	76	1	UINT16		100	kW, 0xFFFF = unsupported
46749	1A5C	6748				NA	36 (P2)	78	1	UINT16		100	kW, 0xFFFF = unsupported
46750	1A5D	6749				NA	38 (P2)	80	1	UINT16		100	kW, 0xFFFF = unsupported
46751	1A5E	6750				NA	40 (P2)	82	1	UINT16		100	kW, 0xFFFF = unsupported
46752	1A5F	6751				NA	42 (P2)	84	1	UINT16		100	kW, 0xFFFF = unsupported
46773	1A74	6772		RO	Minimum Apparent Power Threshold	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			
46773	1A74	6772				1	1 (P1)	1	1	UINT16		100	kW, 0xFFFF = unsupported
46774	1A75	6773				3	3 (P1)	3	1	UINT16		100	kW, 0xFFFF = unsupported
46775	1A76	6774				5	5 (P1)	5	1	UINT16		100	kW, 0xFFFF = unsupported
46776	1A77	6775				7	7 (P1)	7	1	UINT16		100	kW, 0xFFFF = unsupported
46777	1A78	6776				9	9 (P1)	9	1	UINT16		100	kW, 0xFFFF = unsupported
46778	1A79	6777				11	11 (P1)	11	1	UINT16		100	kW, 0xFFFF = unsupported
46779	1A7A	6778				13	13 (P1)	13	1	UINT16		100	kW, 0xFFFF = unsupported
46780	1A7B	6779				15	15 (P1)	15	1	UINT16		100	kW, 0xFFFF = unsupported
46781	1A7C	6780				17	17 (P1)	17	1	UINT16		100	kW, 0xFFFF = unsupported
46782	1A7D	6781				19	19 (P1)	19	1	UINT16		100	kW, 0xFFFF = unsupported
46783	1A7E	6782				21	21 (P1)	21	1	UINT16		100	kW, 0xFFFF = unsupported
46784	1A7F	6783				23	23 (P1)	23	1	UINT16		100	kW, 0xFFFF = unsupported
46785	1A80	6784				25	25 (P1)	25	1	UINT16		100	kW, 0xFFFF = unsupported
46786	1A81	6785				27	27 (P1)	27	1	UINT16		100	kW, 0xFFFF = unsupported
46787	1A82	6786				29	29 (P1)	29	1	UINT16		100	kW, 0xFFFF = unsupported
46788	1A83	6787				31	31 (P1)	31	1	UINT16		100	kW, 0xFFFF = unsupported
46789	1A84	6788				33	33 (P1)	33	1	UINT16		100	kW, 0xFFFF = unsupported
46790	1A85	6789				35	35 (P1)	35	1	UINT16		100	kW, 0xFFFF = unsupported
46791	1A86	6790				37	37 (P1)	37	1	UINT16		100	kW, 0xFFFF = unsupported
46792	1A87	6791				39	39 (P1)	39	1	UINT16		100	kW, 0xFFFF = unsupported
46793	1A88	6792				41	41 (P1)	41	1	UINT16		100	kW, 0xFFFF = unsupported
46814	1A9D	6813				2	2 (P1)	2	1	UINT16		100	kW, 0xFFFF = unsupported
46815	1A9E	6814				4	4 (P1)	4	1	UINT16		100	kW, 0xFFFF = unsupported
46816	1A9F	6815				6	6 (P1)	6	1	UINT16		100	kW, 0xFFFF = unsupported
46817	1AA0	6816				8	8 (P1)	8	1	UINT16		100	kW, 0xFFFF = unsupported
46818	1AA1	6817				10	10 (P1)	10	1	UINT16		100	kW, 0xFFFF = unsupported
46819	1AA2	6818				12	12 (P1)	12	1	UINT16		100	kW, 0xFFFF = unsupported
46820	1AA3	6819				14	14 (P1)	14	1	UINT16		100	kW, 0xFFFF = unsupported
46821	1AA4	6820				16	16 (P1)	16	1	UINT16		100	kW, 0xFFFF = unsupported
46822	1AA5	6821				18	18 (P1)	18	1	UINT16		100	kW, 0xFFFF = unsupported
46823	1AA6	6822				20	20 (P1)	20	1	UINT16		100	kW, 0xFFFF = unsupported
46824	1AA7	6823				22	22 (P1)	22	1	UINT16		100	kW, 0xFFFF = unsupported
46825	1AA8	6824				24	24 (P1)	24	1	UINT16		100	kW, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46826	1AA9	6825				26	26 (P1)	26	1	UINT16		100	kW, 0xFFFF = unsupported
46827	1AAA	6826				28	28 (P1)	28	1	UINT16		100	kW, 0xFFFF = unsupported
46828	1AAB	6827				30	30 (P1)	30	1	UINT16		100	kW, 0xFFFF = unsupported
46829	1AAC	6828				32	32 (P1)	32	1	UINT16		100	kW, 0xFFFF = unsupported
46830	1AAD	6829				34	34 (P1)	34	1	UINT16		100	kW, 0xFFFF = unsupported
46831	1AAE	6830				36	36 (P1)	36	1	UINT16		100	kW, 0xFFFF = unsupported
46832	1AAF	6831				38	38 (P1)	38	1	UINT16		100	kW, 0xFFFF = unsupported
46833	1AB0	6832				40	40 (P1)	40	1	UINT16		100	kW, 0xFFFF = unsupported
46834	1AB1	6833				42	42 (P1)	42	1	UINT16		100	kW, 0xFFFF = unsupported
46855	1AC6	6854				NA	1 (P2)	43	1	UINT16		100	kW, 0xFFFF = unsupported
46856	1AC7	6855				NA	3 (P2)	45	1	UINT16		100	kW, 0xFFFF = unsupported
46857	1AC8	6856				NA	5 (P2)	47	1	UINT16		100	kW, 0xFFFF = unsupported
46858	1AC9	6857				NA	7 (P2)	49	1	UINT16		100	kW, 0xFFFF = unsupported
46859	1ACA	6858				NA	9 (P2)	51	1	UINT16		100	kW, 0xFFFF = unsupported
46860	1ACB	6859				NA	11 (P2)	53	1	UINT16		100	kW, 0xFFFF = unsupported
46861	1ACC	6860				NA	13 (P2)	55	1	UINT16		100	kW, 0xFFFF = unsupported
46862	1ACD	6861				NA	15 (P2)	57	1	UINT16		100	kW, 0xFFFF = unsupported
46863	1ACE	6862				NA	17 (P2)	59	1	UINT16		100	kW, 0xFFFF = unsupported
46864	1ACF	6863				NA	19 (P2)	61	1	UINT16		100	kW, 0xFFFF = unsupported
46865	1AD0	6864				NA	21 (P2)	63	1	UINT16		100	kW, 0xFFFF = unsupported
46866	1AD1	6865				NA	23 (P2)	65	1	UINT16		100	kW, 0xFFFF = unsupported
46867	1AD2	6866				NA	25 (P2)	67	1	UINT16		100	kW, 0xFFFF = unsupported
46868	1AD3	6867				NA	27 (P2)	69	1	UINT16		100	kW, 0xFFFF = unsupported
46869	1AD4	6868				NA	29 (P2)	71	1	UINT16		100	kW, 0xFFFF = unsupported
46870	1AD5	6869				NA	31 (P2)	73	1	UINT16		100	kW, 0xFFFF = unsupported
46871	1AD6	6870				NA	33 (P2)	75	1	UINT16		100	kW, 0xFFFF = unsupported
46872	1AD7	6871				NA	35 (P2)	77	1	UINT16		100	kW, 0xFFFF = unsupported
46873	1AD8	6872				NA	37 (P2)	79	1	UINT16		100	kW, 0xFFFF = unsupported
46874	1AD9	6873				NA	39 (P2)	81	1	UINT16		100	kW, 0xFFFF = unsupported
46875	1ADA	6874				NA	41 (P2)	83	1	UINT16		100	kW, 0xFFFF = unsupported
46896	1AEF	6895				NA	2 (P2)	44	1	UINT16		100	kW, 0xFFFF = unsupported
46897	1AF0	6896				NA	4 (P2)	46	1	UINT16		100	kW, 0xFFFF = unsupported
46898	1AF1	6897				NA	6 (P2)	48	1	UINT16		100	kW, 0xFFFF = unsupported
46899	1AF2	6898				NA	8 (P2)	50	1	UINT16		100	kW, 0xFFFF = unsupported
46900	1AF3	6899				NA	10 (P2)	52	1	UINT16		100	kW, 0xFFFF = unsupported
46901	1AF4	6900				NA	12 (P2)	54	1	UINT16		100	kW, 0xFFFF = unsupported
46902	1AF5	6901				NA	14 (P2)	56	1	UINT16		100	kW, 0xFFFF = unsupported
46903	1AF6	6902				NA	16 (P2)	58	1	UINT16		100	kW, 0xFFFF = unsupported
46904	1AF7	6903				NA	18 (P2)	60	1	UINT16		100	kW, 0xFFFF = unsupported
46905	1AF8	6904				NA	20 (P2)	62	1	UINT16		100	kW, 0xFFFF = unsupported
46906	1AF9	6905				NA	22 (P2)	64	1	UINT16		100	kW, 0xFFFF = unsupported
46907	1AFA	6906				NA	24 (P2)	66	1	UINT16		100	kW, 0xFFFF = unsupported
46908	1AFB	6907				NA	26 (P2)	68	1	UINT16		100	kW, 0xFFFF = unsupported
46909	1AFC	6908				NA	28 (P2)	70	1	UINT16		100	kW, 0xFFFF = unsupported
46910	1AFD	6909				NA	30 (P2)	72	1	UINT16		100	kW, 0xFFFF = unsupported
46911	1AFE	6910				NA	32 (P2)	74	1	UINT16		100	kW, 0xFFFF = unsupported
46912	1AFF	6911				NA	34 (P2)	76	1	UINT16		100	kW, 0xFFFF = unsupported
46913	1B00	6912				NA	36 (P2)	78	1	UINT16		100	kW, 0xFFFF = unsupported
46914	1B01	6913				NA	38 (P2)	80	1	UINT16		100	kW, 0xFFFF = unsupported
46915	1B02	6914				NA	40 (P2)	82	1	UINT16		100	kW, 0xFFFF = unsupported
46916	1B03	6915				NA	42 (P2)	84	1	UINT16		100	kW, 0xFFFF = unsupported
46937	1B18	6936		RO	Maximum Apparent Power Threshold	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	164	UINT16			
46937	1B18	6936				1	1 (P1)	1	1	UINT16		100	kW, 0xFFFF = unsupported
46938	1B19	6937				3	3 (P1)	3	1	UINT16		100	kW, 0xFFFF = unsupported
46939	1B1A	6938				5	5 (P1)	5	1	UINT16		100	kW, 0xFFFF = unsupported
46940	1B1B	6939				7	7 (P1)	7	1	UINT16		100	kW, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
46941	1B1C	6940				9	9 (P1)	9	1	UINT16		100	kW, 0xFFFF = unsupported
46942	1B1D	6941				11	11 (P1)	11	1	UINT16		100	kW, 0xFFFF = unsupported
46943	1B1E	6942				13	13 (P1)	13	1	UINT16		100	kW, 0xFFFF = unsupported
46944	1B1F	6943				15	15 (P1)	15	1	UINT16		100	kW, 0xFFFF = unsupported
46945	1B20	6944				17	17 (P1)	17	1	UINT16		100	kW, 0xFFFF = unsupported
46946	1B21	6945				19	19 (P1)	19	1	UINT16		100	kW, 0xFFFF = unsupported
46947	1B22	6946				21	21 (P1)	21	1	UINT16		100	kW, 0xFFFF = unsupported
46948	1B23	6947				23	23 (P1)	23	1	UINT16		100	kW, 0xFFFF = unsupported
46949	1B24	6948				25	25 (P1)	25	1	UINT16		100	kW, 0xFFFF = unsupported
46950	1B25	6949				27	27 (P1)	27	1	UINT16		100	kW, 0xFFFF = unsupported
46951	1B26	6950				29	29 (P1)	29	1	UINT16		100	kW, 0xFFFF = unsupported
46952	1B27	6951				31	31 (P1)	31	1	UINT16		100	kW, 0xFFFF = unsupported
46953	1B28	6952				33	33 (P1)	33	1	UINT16		100	kW, 0xFFFF = unsupported
46954	1B29	6953				35	35 (P1)	35	1	UINT16		100	kW, 0xFFFF = unsupported
46955	1B2A	6954				37	37 (P1)	37	1	UINT16		100	kW, 0xFFFF = unsupported
46956	1B2B	6955				39	39 (P1)	39	1	UINT16		100	kW, 0xFFFF = unsupported
46957	1B2C	6956				41	41 (P1)	41	1	UINT16		100	kW, 0xFFFF = unsupported
46978	1B41	6977				2	2 (P1)	2	1	UINT16		100	kW, 0xFFFF = unsupported
46979	1B42	6978				4	4 (P1)	4	1	UINT16		100	kW, 0xFFFF = unsupported
46980	1B43	6979				6	6 (P1)	6	1	UINT16		100	kW, 0xFFFF = unsupported
46981	1B44	6980				8	8 (P1)	8	1	UINT16		100	kW, 0xFFFF = unsupported
46982	1B45	6981				10	10 (P1)	10	1	UINT16		100	kW, 0xFFFF = unsupported
46983	1B46	6982				12	12 (P1)	12	1	UINT16		100	kW, 0xFFFF = unsupported
46984	1B47	6983				14	14 (P1)	14	1	UINT16		100	kW, 0xFFFF = unsupported
46985	1B48	6984				16	16 (P1)	16	1	UINT16		100	kW, 0xFFFF = unsupported
46986	1B49	6985				18	18 (P1)	18	1	UINT16		100	kW, 0xFFFF = unsupported
46987	1B4A	6986				20	20 (P1)	20	1	UINT16		100	kW, 0xFFFF = unsupported
46988	1B4B	6987				22	22 (P1)	22	1	UINT16		100	kW, 0xFFFF = unsupported
46989	1B4C	6988				24	24 (P1)	24	1	UINT16		100	kW, 0xFFFF = unsupported
46990	1B4D	6989				26	26 (P1)	26	1	UINT16		100	kW, 0xFFFF = unsupported
46991	1B4E	6990				28	28 (P1)	28	1	UINT16		100	kW, 0xFFFF = unsupported
46992	1B4F	6991				30	30 (P1)	30	1	UINT16		100	kW, 0xFFFF = unsupported
46993	1B50	6992				32	32 (P1)	32	1	UINT16		100	kW, 0xFFFF = unsupported
46994	1B51	6993				34	34 (P1)	34	1	UINT16		100	kW, 0xFFFF = unsupported
46995	1B52	6994				36	36 (P1)	36	1	UINT16		100	kW, 0xFFFF = unsupported
46996	1B53	6995				38	38 (P1)	38	1	UINT16		100	kW, 0xFFFF = unsupported
46997	1B54	6996				40	40 (P1)	40	1	UINT16		100	kW, 0xFFFF = unsupported
46998	1B55	6997				42	42 (P1)	42	1	UINT16		100	kW, 0xFFFF = unsupported
47019	1B6A	7018				NA	1 (P2)	43	1	UINT16		100	kW, 0xFFFF = unsupported
47020	1B6B	7019				NA	3 (P2)	45	1	UINT16		100	kW, 0xFFFF = unsupported
47021	1B6C	7020				NA	5 (P2)	47	1	UINT16		100	kW, 0xFFFF = unsupported
47022	1B6D	7021				NA	7 (P2)	49	1	UINT16		100	kW, 0xFFFF = unsupported
47023	1B6E	7022				NA	9 (P2)	51	1	UINT16		100	kW, 0xFFFF = unsupported
47024	1B6F	7023				NA	11 (P2)	53	1	UINT16		100	kW, 0xFFFF = unsupported
47025	1B70	7024				NA	13 (P2)	55	1	UINT16		100	kW, 0xFFFF = unsupported
47026	1B71	7025				NA	15 (P2)	57	1	UINT16		100	kW, 0xFFFF = unsupported
47027	1B72	7026				NA	17 (P2)	59	1	UINT16		100	kW, 0xFFFF = unsupported
47028	1B73	7027				NA	19 (P2)	61	1	UINT16		100	kW, 0xFFFF = unsupported
47029	1B74	7028				NA	21 (P2)	63	1	UINT16		100	kW, 0xFFFF = unsupported
47030	1B75	7029				NA	23 (P2)	65	1	UINT16		100	kW, 0xFFFF = unsupported
47031	1B76	7030				NA	25 (P2)	67	1	UINT16		100	kW, 0xFFFF = unsupported
47032	1B77	7031				NA	27 (P2)	69	1	UINT16		100	kW, 0xFFFF = unsupported
47033	1B78	7032				NA	29 (P2)	71	1	UINT16		100	kW, 0xFFFF = unsupported
47034	1B79	7033				NA	31 (P2)	73	1	UINT16		100	kW, 0xFFFF = unsupported
47035	1B7A	7034				NA	33 (P2)	75	1	UINT16		100	kW, 0xFFFF = unsupported
47036	1B7B	7035				NA	35 (P2)	77	1	UINT16		100	kW, 0xFFFF = unsupported
47037	1B7C	7036				NA	37 (P2)	79	1	UINT16		100	kW, 0xFFFF = unsupported
47038	1B7D	7037				NA	39 (P2)	81	1	UINT16		100	kW, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
47039	1B7E	7038				NA	41 (P2)	83	1	UINT16		100	kW, 0xFFFF = unsupported
47060	1B93	7059				NA	2 (P2)	44	1	UINT16		100	kW, 0xFFFF = unsupported
47061	1B94	7060				NA	4 (P2)	46	1	UINT16		100	kW, 0xFFFF = unsupported
47062	1B95	7061				NA	6 (P2)	48	1	UINT16		100	kW, 0xFFFF = unsupported
47063	1B96	7062				NA	8 (P2)	50	1	UINT16		100	kW, 0xFFFF = unsupported
47064	1B97	7063				NA	10 (P2)	52	1	UINT16		100	kW, 0xFFFF = unsupported
47065	1B98	7064				NA	12 (P2)	54	1	UINT16		100	kW, 0xFFFF = unsupported
47066	1B99	7065				NA	14 (P2)	56	1	UINT16		100	kW, 0xFFFF = unsupported
47067	1B9A	7066				NA	16 (P2)	58	1	UINT16		100	kW, 0xFFFF = unsupported
47068	1B9B	7067				NA	18 (P2)	60	1	UINT16		100	kW, 0xFFFF = unsupported
47069	1B9C	7068				NA	20 (P2)	62	1	UINT16		100	kW, 0xFFFF = unsupported
47070	1B9D	7069				NA	22 (P2)	64	1	UINT16		100	kW, 0xFFFF = unsupported
47071	1B9E	7070				NA	24 (P2)	66	1	UINT16		100	kW, 0xFFFF = unsupported
47072	1B9F	7071				NA	26 (P2)	68	1	UINT16		100	kW, 0xFFFF = unsupported
47073	1BA0	7072				NA	28 (P2)	70	1	UINT16		100	kW, 0xFFFF = unsupported
47074	1BA1	7073				NA	30 (P2)	72	1	UINT16		100	kW, 0xFFFF = unsupported
47075	1BA2	7074				NA	32 (P2)	74	1	UINT16		100	kW, 0xFFFF = unsupported
47076	1BA3	7075				NA	34 (P2)	76	1	UINT16		100	kW, 0xFFFF = unsupported
47077	1BA4	7076				NA	36 (P2)	78	1	UINT16		100	kW, 0xFFFF = unsupported
47078	1BA5	7077				NA	38 (P2)	80	1	UINT16		100	kW, 0xFFFF = unsupported
47079	1BA6	7078				NA	40 (P2)	82	1	UINT16		100	kW, 0xFFFF = unsupported
47080	1BA7	7079				NA	42 (P2)	84	1	UINT16		100	kW, 0xFFFF = unsupported
47101	1BBC	7100		RO	Energy Usage Reset Date	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	1640	ASCII			
47101	1BBC	7100				1	1 (P1)	1	10	ASCII			character string, nosupport = if MCM unsupported
47111	1BC6	7110				3	3 (P1)	3	10	ASCII			character string, nosupport = if MCM unsupported
47121	1BD0	7120				5	5 (P1)	5	10	ASCII			character string, nosupport = if MCM unsupported
47131	1BDA	7130				7	7 (P1)	7	10	ASCII			character string, nosupport = if MCM unsupported
47141	1BE4	7140				9	9 (P1)	9	10	ASCII			character string, nosupport = if MCM unsupported
47151	1BEE	7150				11	11 (P1)	11	10	ASCII			character string, nosupport = if MCM unsupported
47161	1BF8	7160				13	13 (P1)	13	10	ASCII			character string, nosupport = if MCM unsupported
47171	1C02	7170				15	15 (P1)	15	10	ASCII			character string, nosupport = if MCM unsupported
47181	1C0C	7180				17	17 (P1)	17	10	ASCII			character string, nosupport = if MCM unsupported
47191	1C16	7190				19	19 (P1)	19	10	ASCII			character string, nosupport = if MCM unsupported
47201	1C20	7200				21	21 (P1)	21	10	ASCII			character string, nosupport = if MCM unsupported
47211	1C2A	7210				23	23 (P1)	23	10	ASCII			character string, nosupport = if MCM unsupported
47221	1C34	7220				25	25 (P1)	25	10	ASCII			character string, nosupport = if MCM unsupported
47231	1C3E	7230				27	27 (P1)	27	10	ASCII			character string, nosupport = if MCM unsupported
47241	1C48	7240				29	29 (P1)	29	10	ASCII			character string, nosupport = if MCM unsupported
47251	1C52	7250				31	31 (P1)	31	10	ASCII			character string, nosupport = if MCM unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
47261	1C5C	7260				33	33 (P1)	33	10	ASCII			character string, nosupport = if MCM unsupported
47271	1C66	7270				35	35 (P1)	35	10	ASCII			character string, nosupport = if MCM unsupported
47281	1C70	7280				37	37 (P1)	37	10	ASCII			character string, nosupport = if MCM unsupported
47291	1C7A	7290				39	39 (P1)	39	10	ASCII			character string, nosupport = if MCM unsupported
47301	1C84	7300				41	41 (P1)	41	10	ASCII			character string, nosupport = if MCM unsupported
47511	1D56	7510				2	2 (P1)	2	10	ASCII			character string, nosupport = if MCM unsupported
47521	1D60	7520				4	4 (P1)	4	10	ASCII			character string, nosupport = if MCM unsupported
47531	1D6A	7530				6	6 (P1)	6	10	ASCII			character string, nosupport = if MCM unsupported
47541	1D74	7540				8	8 (P1)	8	10	ASCII			character string, nosupport = if MCM unsupported
47551	1D7E	7550				10	10 (P1)	10	10	ASCII			character string, nosupport = if MCM unsupported
47561	1D88	7560				12	12 (P1)	12	10	ASCII			character string, nosupport = if MCM unsupported
47571	1D92	7570				14	14 (P1)	14	10	ASCII			character string, nosupport = if MCM unsupported
47581	1D9C	7580				16	16 (P1)	16	10	ASCII			character string, nosupport = if MCM unsupported
47591	1DA6	7590				18	18 (P1)	18	10	ASCII			character string, nosupport = if MCM unsupported
47601	1DB0	7600				20	20 (P1)	20	10	ASCII			character string, nosupport = if MCM unsupported
47611	1DBA	7610				22	22 (P1)	22	10	ASCII			character string, nosupport = if MCM unsupported
47621	1DC4	7620				24	24 (P1)	24	10	ASCII			character string, nosupport = if MCM unsupported
47631	1DCE	7630				26	26 (P1)	26	10	ASCII			character string, nosupport = if MCM unsupported
47641	1DD8	7640				28	28 (P1)	28	10	ASCII			character string, nosupport = if MCM unsupported
47651	1DE2	7650				30	30 (P1)	30	10	ASCII			character string, nosupport = if MCM unsupported
47661	1DEC	7660				32	32 (P1)	32	10	ASCII			character string, nosupport = if MCM unsupported
47671	1DF6	7670				34	34 (P1)	34	10	ASCII			character string, nosupport = if MCM unsupported
47681	1E00	7680				36	36 (P1)	36	10	ASCII			character string, nosupport = if MCM unsupported
47691	1E0A	7690				38	38 (P1)	38	10	ASCII			character string, nosupport = if MCM unsupported
47701	1E14	7700				40	40 (P1)	40	10	ASCII			character string, nosupport = if MCM unsupported
47711	1E1E	7710				42	42 (P1)	42	10	ASCII			character string, nosupport = if MCM unsupported
47921	1EF0	7920				NA	1 (P2)	43	10	ASCII			character string, nosupport = if MCM unsupported
47931	1EFA	7930				NA	3 (P2)	45	10	ASCII			character string, nosupport = if MCM unsupported
47941	1F04	7940				NA	5 (P2)	47	10	ASCII			character string, nosupport = if MCM unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
47951	1F0E	7950				NA	7 (P2)	49	10	ASCII			character string, nosupport = if MCM unsupported
47961	1F18	7960				NA	9 (P2)	51	10	ASCII			character string, nosupport = if MCM unsupported
47971	1F22	7970				NA	11 (P2)	53	10	ASCII			character string, nosupport = if MCM unsupported
47981	1F2C	7980				NA	13 (P2)	55	10	ASCII			character string, nosupport = if MCM unsupported
47991	1F36	7990				NA	15 (P2)	57	10	ASCII			character string, nosupport = if MCM unsupported
48001	1F40	8000				NA	17 (P2)	59	10	ASCII			character string, nosupport = if MCM unsupported
48011	1F4A	8010				NA	19 (P2)	61	10	ASCII			character string, nosupport = if MCM unsupported
48021	1F54	8020				NA	21 (P2)	63	10	ASCII			character string, nosupport = if MCM unsupported
48031	1F5E	8030				NA	23 (P2)	65	10	ASCII			character string, nosupport = if MCM unsupported
48041	1F68	8040				NA	25 (P2)	67	10	ASCII			character string, nosupport = if MCM unsupported
48051	1F72	8050				NA	27 (P2)	69	10	ASCII			character string, nosupport = if MCM unsupported
48061	1F7C	8060				NA	29 (P2)	71	10	ASCII			character string, nosupport = if MCM unsupported
48071	1F86	8070				NA	31 (P2)	73	10	ASCII			character string, nosupport = if MCM unsupported
48081	1F90	8080				NA	33 (P2)	75	10	ASCII			character string, nosupport = if MCM unsupported
48091	1F9A	8090				NA	35 (P2)	77	10	ASCII			character string, nosupport = if MCM unsupported
48101	1FA4	8100				NA	37 (P2)	79	10	ASCII			character string, nosupport = if MCM unsupported
48111	1FAE	8110				NA	39 (P2)	81	10	ASCII			character string, nosupport = if MCM unsupported
48121	1FB8	8120				NA	41 (P2)	83	10	ASCII			character string, nosupport = if MCM unsupported
48331	208A	8330				NA	2 (P2)	44	10	ASCII			character string, nosupport = if MCM unsupported
48341	2094	8340				NA	4 (P2)	46	10	ASCII			character string, nosupport = if MCM unsupported
48351	209E	8350				NA	6 (P2)	48	10	ASCII			character string, nosupport = if MCM unsupported
48361	20A8	8360				NA	8 (P2)	50	10	ASCII			character string, nosupport = if MCM unsupported
48371	20B2	8370				NA	10 (P2)	52	10	ASCII			character string, nosupport = if MCM unsupported
48381	20BC	8380				NA	12 (P2)	54	10	ASCII			character string, nosupport = if MCM unsupported
48391	20C6	8390				NA	14 (P2)	56	10	ASCII			character string, nosupport = if MCM unsupported
48401	20D0	8400				NA	16 (P2)	58	10	ASCII			character string, nosupport = if MCM unsupported
48411	20DA	8410				NA	18 (P2)	60	10	ASCII			character string, nosupport = if MCM unsupported
48421	20E4	8420				NA	20 (P2)	62	10	ASCII			character string, nosupport = if MCM unsupported
48431	20EE	8430				NA	22 (P2)	64	10	ASCII			character string, nosupport = if MCM unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
48441	20F8	8440				NA	24 (P2)	66	10	ASCII			character string, nosupport = if MCM unsupported
48451	2102	8450				NA	26 (P2)	68	10	ASCII			character string, nosupport = if MCM unsupported
48461	210C	8460				NA	28 (P2)	70	10	ASCII			character string, nosupport = if MCM unsupported
48471	2116	8470				NA	30 (P2)	72	10	ASCII			character string, nosupport = if MCM unsupported
48481	2120	8480				NA	32 (P2)	74	10	ASCII			character string, nosupport = if MCM unsupported
48491	212A	8490				NA	34 (P2)	76	10	ASCII			character string, nosupport = if MCM unsupported
48501	2134	8500				NA	36 (P2)	78	10	ASCII			character string, nosupport = if MCM unsupported
48511	213E	8510				NA	38 (P2)	80	10	ASCII			character string, nosupport = if MCM unsupported
48521	2148	8520				NA	40 (P2)	82	10	ASCII			character string, nosupport = if MCM unsupported
48531	2152	8530				NA	42 (P2)	84	10	ASCII			character string, nosupport = if MCM unsupported
	2224	8740											
	<b>4E20</b>	<b>20000</b>	<b>4.2 Measurements</b>										
60001	4E20	20000		RO	Active Power Measurements	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	84	UINT16			
60001	4E20	20000				1	1 (P1)	1	1	UINT16		10	kW, 0xFFFF = unsupported
60002	4E21	20001				3	3 (P1)	3	1	UINT16		10	kW, 0xFFFF = unsupported
60003	4E22	20002				5	5 (P1)	5	1	UINT16		10	kW, 0xFFFF = unsupported
60004	4E23	20003				7	7 (P1)	7	1	UINT16		10	kW, 0xFFFF = unsupported
60005	4E24	20004				9	9 (P1)	9	1	UINT16		10	kW, 0xFFFF = unsupported
60006	4E25	20005				11	11 (P1)	11	1	UINT16		10	kW, 0xFFFF = unsupported
60007	4E26	20006				13	13 (P1)	13	1	UINT16		10	kW, 0xFFFF = unsupported
60008	4E27	20007				15	15 (P1)	15	1	UINT16		10	kW, 0xFFFF = unsupported
60009	4E28	20008				17	17 (P1)	17	1	UINT16		10	kW, 0xFFFF = unsupported
60010	4E29	20009				19	19 (P1)	19	1	UINT16		10	kW, 0xFFFF = unsupported
60011	4E2A	20010				21	21 (P1)	21	1	UINT16		10	kW, 0xFFFF = unsupported
60012	4E2B	20011				23	23 (P1)	23	1	UINT16		10	kW, 0xFFFF = unsupported
60013	4E2C	20012				25	25 (P1)	25	1	UINT16		10	kW, 0xFFFF = unsupported
60014	4E2D	20013				27	27 (P1)	27	1	UINT16		10	kW, 0xFFFF = unsupported
60015	4E2E	20014				29	29 (P1)	29	1	UINT16		10	kW, 0xFFFF = unsupported
60016	4E2F	20015				31	31 (P1)	31	1	UINT16		10	kW, 0xFFFF = unsupported
60017	4E30	20016				33	33 (P1)	33	1	UINT16		10	kW, 0xFFFF = unsupported
60018	4E31	20017				35	35 (P1)	35	1	UINT16		10	kW, 0xFFFF = unsupported
60019	4E32	20018				37	37 (P1)	37	1	UINT16		10	kW, 0xFFFF = unsupported
60020	4E33	20019				39	39 (P1)	39	1	UINT16		10	kW, 0xFFFF = unsupported
60021	4E34	20020				41	41 (P1)	41	1	UINT16		10	kW, 0xFFFF = unsupported
60042	4E49	20041				2	2 (P1)	2	1	UINT16		10	kW, 0xFFFF = unsupported
60043	4E4A	20042				4	4 (P1)	4	1	UINT16		10	kW, 0xFFFF = unsupported
60044	4E4B	20043				6	6 (P1)	6	1	UINT16		10	kW, 0xFFFF = unsupported
60045	4E4C	20044				8	8 (P1)	8	1	UINT16		10	kW, 0xFFFF = unsupported
60046	4E4D	20045				10	10 (P1)	10	1	UINT16		10	kW, 0xFFFF = unsupported
60047	4E4E	20046				12	12 (P1)	12	1	UINT16		10	kW, 0xFFFF = unsupported
60048	4E4F	20047				14	14 (P1)	14	1	UINT16		10	kW, 0xFFFF = unsupported
60049	4E50	20048				16	16 (P1)	16	1	UINT16		10	kW, 0xFFFF = unsupported
60050	4E51	20049				18	18 (P1)	18	1	UINT16		10	kW, 0xFFFF = unsupported
60051	4E52	20050				20	20 (P1)	20	1	UINT16		10	kW, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60052	4E53	20051				22	22 (P1)	22	1	UINT16		10	kW, 0xFFFF = unsupported
60053	4E54	20052				24	24 (P1)	24	1	UINT16		10	kW, 0xFFFF = unsupported
60054	4E55	20053				26	26 (P1)	26	1	UINT16		10	kW, 0xFFFF = unsupported
60055	4E56	20054				28	28 (P1)	28	1	UINT16		10	kW, 0xFFFF = unsupported
60056	4E57	20055				30	30 (P1)	30	1	UINT16		10	kW, 0xFFFF = unsupported
60057	4E58	20056				32	32 (P1)	32	1	UINT16		10	kW, 0xFFFF = unsupported
60058	4E59	20057				34	34 (P1)	34	1	UINT16		10	kW, 0xFFFF = unsupported
60059	4E5A	20058				36	36 (P1)	36	1	UINT16		10	kW, 0xFFFF = unsupported
60060	4E5B	20059				38	38 (P1)	38	1	UINT16		10	kW, 0xFFFF = unsupported
60061	4E5C	20060				40	40 (P1)	40	1	UINT16		10	kW, 0xFFFF = unsupported
60062	4E5D	20061				42	42 (P1)	42	1	UINT16		10	kW, 0xFFFF = unsupported
60083	4E72	20082				NA	1 (P2)	43	1	UINT16		10	kW, 0xFFFF = unsupported
60084	4E73	20083				NA	3 (P2)	45	1	UINT16		10	kW, 0xFFFF = unsupported
60085	4E74	20084				NA	5 (P2)	47	1	UINT16		10	kW, 0xFFFF = unsupported
60086	4E75	20085				NA	7 (P2)	49	1	UINT16		10	kW, 0xFFFF = unsupported
60087	4E76	20086				NA	9 (P2)	51	1	UINT16		10	kW, 0xFFFF = unsupported
60088	4E77	20087				NA	11 (P2)	53	1	UINT16		10	kW, 0xFFFF = unsupported
60089	4E78	20088				NA	13 (P2)	55	1	UINT16		10	kW, 0xFFFF = unsupported
60090	4E79	20089				NA	15 (P2)	57	1	UINT16		10	kW, 0xFFFF = unsupported
60091	4E7A	20090				NA	17 (P2)	59	1	UINT16		10	kW, 0xFFFF = unsupported
60092	4E7B	20091				NA	19 (P2)	61	1	UINT16		10	kW, 0xFFFF = unsupported
60093	4E7C	20092				NA	21 (P2)	63	1	UINT16		10	kW, 0xFFFF = unsupported
60094	4E7D	20093				NA	23 (P2)	65	1	UINT16		10	kW, 0xFFFF = unsupported
60095	4E7E	20094				NA	25 (P2)	67	1	UINT16		10	kW, 0xFFFF = unsupported
60096	4E7F	20095				NA	27 (P2)	69	1	UINT16		10	kW, 0xFFFF = unsupported
60097	4E80	20096				NA	29 (P2)	71	1	UINT16		10	kW, 0xFFFF = unsupported
60098	4E81	20097				NA	31 (P2)	73	1	UINT16		10	kW, 0xFFFF = unsupported
60099	4E82	20098				NA	33 (P2)	75	1	UINT16		10	kW, 0xFFFF = unsupported
60100	4E83	20099				NA	35 (P2)	77	1	UINT16		10	kW, 0xFFFF = unsupported
60101	4E84	20100				NA	37 (P2)	79	1	UINT16		10	kW, 0xFFFF = unsupported
60102	4E85	20101				NA	39 (P2)	81	1	UINT16		10	kW, 0xFFFF = unsupported
60103	4E86	20102				NA	41 (P2)	83	1	UINT16		10	kW, 0xFFFF = unsupported
60124	4E9B	20123				NA	2 (P2)	44	1	UINT16		10	kW, 0xFFFF = unsupported
60125	4E9C	20124				NA	4 (P2)	46	1	UINT16		10	kW, 0xFFFF = unsupported
60126	4E9D	20125				NA	6 (P2)	48	1	UINT16		10	kW, 0xFFFF = unsupported
60127	4E9E	20126				NA	8 (P2)	50	1	UINT16		10	kW, 0xFFFF = unsupported
60128	4E9F	20127				NA	10 (P2)	52	1	UINT16		10	kW, 0xFFFF = unsupported
60129	4EA0	20128				NA	12 (P2)	54	1	UINT16		10	kW, 0xFFFF = unsupported
60130	4EA1	20129				NA	14 (P2)	56	1	UINT16		10	kW, 0xFFFF = unsupported
60131	4EA2	20130				NA	16 (P2)	58	1	UINT16		10	kW, 0xFFFF = unsupported
60132	4EA3	20131				NA	18 (P2)	60	1	UINT16		10	kW, 0xFFFF = unsupported
60133	4EA4	20132				NA	20 (P2)	62	1	UINT16		10	kW, 0xFFFF = unsupported
60134	4EA5	20133				NA	22 (P2)	64	1	UINT16		10	kW, 0xFFFF = unsupported
60135	4EA6	20134				NA	24 (P2)	66	1	UINT16		10	kW, 0xFFFF = unsupported
60136	4EA7	20135				NA	26 (P2)	68	1	UINT16		10	kW, 0xFFFF = unsupported
60137	4EA8	20136				NA	28 (P2)	70	1	UINT16		10	kW, 0xFFFF = unsupported
60138	4EA9	20137				NA	30 (P2)	72	1	UINT16		10	kW, 0xFFFF = unsupported
60139	4EAA	20138				NA	32 (P2)	74	1	UINT16		10	kW, 0xFFFF = unsupported
60140	4EAB	20139				NA	34 (P2)	76	1	UINT16		10	kW, 0xFFFF = unsupported
60141	4EAC	20140				NA	36 (P2)	78	1	UINT16		10	kW, 0xFFFF = unsupported
60142	4EAD	20141				NA	38 (P2)	80	1	UINT16		10	kW, 0xFFFF = unsupported
60143	4EAE	20142				NA	40 (P2)	82	1	UINT16		10	kW, 0xFFFF = unsupported
60144	4EAF	20143				NA	42 (P2)	84	1	UINT16		10	kW, 0xFFFF = unsupported
60165	4EC4	20164		RO	Apparent Power Measurements	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	84	UINT16			
60165	4EC4	20164				1	1 (P1)	1	1	UINT16		100	kVA, 0xFFFF = unsupported
60166	4EC5	20165				3	3 (P1)	3	1	UINT16		100	kVA, 0xFFFF = unsupported



Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60167	4EC6	20166				5	5 (P1)	5	1	UINT16		100	kVA, 0xFFFF = unsupported
60168	4EC7	20167				7	7 (P1)	7	1	UINT16		100	kVA, 0xFFFF = unsupported
60169	4EC8	20168				9	9 (P1)	9	1	UINT16		100	kVA, 0xFFFF = unsupported
60170	4EC9	20169				11	11 (P1)	11	1	UINT16		100	kVA, 0xFFFF = unsupported
60171	4ECA	20170				13	13 (P1)	13	1	UINT16		100	kVA, 0xFFFF = unsupported
60172	4ECB	20171				15	15 (P1)	15	1	UINT16		100	kVA, 0xFFFF = unsupported
60173	4ECC	20172				17	17 (P1)	17	1	UINT16		100	kVA, 0xFFFF = unsupported
60174	4ECD	20173				19	19 (P1)	19	1	UINT16		100	kVA, 0xFFFF = unsupported
60175	4ECE	20174				21	21 (P1)	21	1	UINT16		100	kVA, 0xFFFF = unsupported
60176	4ECF	20175				23	23 (P1)	23	1	UINT16		100	kVA, 0xFFFF = unsupported
60177	4ED0	20176				25	25 (P1)	25	1	UINT16		100	kVA, 0xFFFF = unsupported
60178	4ED1	20177				27	27 (P1)	27	1	UINT16		100	kVA, 0xFFFF = unsupported
60179	4ED2	20178				29	29 (P1)	29	1	UINT16		100	kVA, 0xFFFF = unsupported
60180	4ED3	20179				31	31 (P1)	31	1	UINT16		100	kVA, 0xFFFF = unsupported
60181	4ED4	20180				33	33 (P1)	33	1	UINT16		100	kVA, 0xFFFF = unsupported
60182	4ED5	20181				35	35 (P1)	35	1	UINT16		100	kVA, 0xFFFF = unsupported
60183	4ED6	20182				37	37 (P1)	37	1	UINT16		100	kVA, 0xFFFF = unsupported
60184	4ED7	20183				39	39 (P1)	39	1	UINT16		100	kVA, 0xFFFF = unsupported
60185	4ED8	20184				41	41 (P1)	41	1	UINT16		100	kVA, 0xFFFF = unsupported
60206	4EED	20205				2	2 (P1)	2	1	UINT16		100	kVA, 0xFFFF = unsupported
60207	4EEE	20206				4	4 (P1)	4	1	UINT16		100	kVA, 0xFFFF = unsupported
60208	4EEF	20207				6	6 (P1)	6	1	UINT16		100	kVA, 0xFFFF = unsupported
60209	4EF0	20208				8	8 (P1)	8	1	UINT16		100	kVA, 0xFFFF = unsupported
60210	4EF1	20209				10	10 (P1)	10	1	UINT16		100	kVA, 0xFFFF = unsupported
60211	4EF2	20210				12	12 (P1)	12	1	UINT16		100	kVA, 0xFFFF = unsupported
60212	4EF3	20211				14	14 (P1)	14	1	UINT16		100	kVA, 0xFFFF = unsupported
60213	4EF4	20212				16	16 (P1)	16	1	UINT16		100	kVA, 0xFFFF = unsupported
60214	4EF5	20213				18	18 (P1)	18	1	UINT16		100	kVA, 0xFFFF = unsupported
60215	4EF6	20214				20	20 (P1)	20	1	UINT16		100	kVA, 0xFFFF = unsupported
60216	4EF7	20215				22	22 (P1)	22	1	UINT16		100	kVA, 0xFFFF = unsupported
60217	4EF8	20216				24	24 (P1)	24	1	UINT16		100	kVA, 0xFFFF = unsupported
60218	4EF9	20217				26	26 (P1)	26	1	UINT16		100	kVA, 0xFFFF = unsupported
60219	4EFA	20218				28	28 (P1)	28	1	UINT16		100	kVA, 0xFFFF = unsupported
60220	4EFB	20219				30	30 (P1)	30	1	UINT16		100	kVA, 0xFFFF = unsupported
60221	4EFC	20220				32	32 (P1)	32	1	UINT16		100	kVA, 0xFFFF = unsupported
60222	4EFD	20221				34	34 (P1)	34	1	UINT16		100	kVA, 0xFFFF = unsupported
60223	4EFE	20222				36	36 (P1)	36	1	UINT16		100	kVA, 0xFFFF = unsupported
60224	4EFF	20223				38	38 (P1)	38	1	UINT16		100	kVA, 0xFFFF = unsupported
60225	4F00	20224				40	40 (P1)	40	1	UINT16		100	kVA, 0xFFFF = unsupported
60226	4F01	20225				42	42 (P1)	42	1	UINT16		100	kVA, 0xFFFF = unsupported
60247	4F16	20246				NA	1 (P2)	43	1	UINT16		100	kVA, 0xFFFF = unsupported
60248	4F17	20247				NA	3 (P2)	45	1	UINT16		100	kVA, 0xFFFF = unsupported
60249	4F18	20248				NA	5 (P2)	47	1	UINT16		100	kVA, 0xFFFF = unsupported
60250	4F19	20249				NA	7 (P2)	49	1	UINT16		100	kVA, 0xFFFF = unsupported
60251	4F1A	20250				NA	9 (P2)	51	1	UINT16		100	kVA, 0xFFFF = unsupported
60252	4F1B	20251				NA	11 (P2)	53	1	UINT16		100	kVA, 0xFFFF = unsupported
60253	4F1C	20252				NA	13 (P2)	55	1	UINT16		100	kVA, 0xFFFF = unsupported
60254	4F1D	20253				NA	15 (P2)	57	1	UINT16		100	kVA, 0xFFFF = unsupported
60255	4F1E	20254				NA	17 (P2)	59	1	UINT16		100	kVA, 0xFFFF = unsupported
60256	4F1F	20255				NA	19 (P2)	61	1	UINT16		100	kVA, 0xFFFF = unsupported
60257	4F20	20256				NA	21 (P2)	63	1	UINT16		100	kVA, 0xFFFF = unsupported
60258	4F21	20257				NA	23 (P2)	65	1	UINT16		100	kVA, 0xFFFF = unsupported
60259	4F22	20258				NA	25 (P2)	67	1	UINT16		100	kVA, 0xFFFF = unsupported
60260	4F23	20259				NA	27 (P2)	69	1	UINT16		100	kVA, 0xFFFF = unsupported
60261	4F24	20260				NA	29 (P2)	71	1	UINT16		100	kVA, 0xFFFF = unsupported
60262	4F25	20261				NA	31 (P2)	73	1	UINT16		100	kVA, 0xFFFF = unsupported
60263	4F26	20262				NA	33 (P2)	75	1	UINT16		100	kVA, 0xFFFF = unsupported
60264	4F27	20263				NA	35 (P2)	77	1	UINT16		100	kVA, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60265	4F28	20264				NA	37 (P2)	79	1	UINT16		100	kVA, 0xFFFF = unsupported
60266	4F29	20265				NA	39 (P2)	81	1	UINT16		100	kVA, 0xFFFF = unsupported
60267	4F2A	20266				NA	41 (P2)	83	1	UINT16		100	kVA, 0xFFFF = unsupported
60288	4F3F	20287				NA	2 (P2)	44	1	UINT16		100	kVA, 0xFFFF = unsupported
60289	4F40	20288				NA	4 (P2)	46	1	UINT16		100	kVA, 0xFFFF = unsupported
60290	4F41	20289				NA	6 (P2)	48	1	UINT16		100	kVA, 0xFFFF = unsupported
60291	4F42	20290				NA	8 (P2)	50	1	UINT16		100	kVA, 0xFFFF = unsupported
60292	4F43	20291				NA	10 (P2)	52	1	UINT16		100	kVA, 0xFFFF = unsupported
60293	4F44	20292				NA	12 (P2)	54	1	UINT16		100	kVA, 0xFFFF = unsupported
60294	4F45	20293				NA	14 (P2)	56	1	UINT16		100	kVA, 0xFFFF = unsupported
60295	4F46	20294				NA	16 (P2)	58	1	UINT16		100	kVA, 0xFFFF = unsupported
60296	4F47	20295				NA	18 (P2)	60	1	UINT16		100	kVA, 0xFFFF = unsupported
60297	4F48	20296				NA	20 (P2)	62	1	UINT16		100	kVA, 0xFFFF = unsupported
60298	4F49	20297				NA	22 (P2)	64	1	UINT16		100	kVA, 0xFFFF = unsupported
60299	4F4A	20298				NA	24 (P2)	66	1	UINT16		100	kVA, 0xFFFF = unsupported
60300	4F4B	20299				NA	26 (P2)	68	1	UINT16		100	kVA, 0xFFFF = unsupported
60301	4F4C	20300				NA	28 (P2)	70	1	UINT16		100	kVA, 0xFFFF = unsupported
60302	4F4D	20301				NA	30 (P2)	72	1	UINT16		100	kVA, 0xFFFF = unsupported
60303	4F4E	20302				NA	32 (P2)	74	1	UINT16		100	kVA, 0xFFFF = unsupported
60304	4F4F	20303				NA	34 (P2)	76	1	UINT16		100	kVA, 0xFFFF = unsupported
60305	4F50	20304				NA	36 (P2)	78	1	UINT16		100	kVA, 0xFFFF = unsupported
60306	4F51	20305				NA	38 (P2)	80	1	UINT16		100	kVA, 0xFFFF = unsupported
60307	4F52	20306				NA	40 (P2)	82	1	UINT16		100	kVA, 0xFFFF = unsupported
60308	4F53	20307				NA	42 (P2)	84	1	UINT16		100	kVA, 0xFFFF = unsupported
60493	500C	20492		RO	Power Factor	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	84	INT16			
60493	500C	20492				1	1 (P1)	1	1	INT16		100	0xFFFF = unsupported
60494	500D	20493				3	3 (P1)	3	1	INT16		100	0xFFFF = unsupported
60495	500E	20494				5	5 (P1)	5	1	INT16		100	0xFFFF = unsupported
60496	500F	20495				7	7 (P1)	7	1	INT16		100	0xFFFF = unsupported
60497	5010	20496				9	9 (P1)	9	1	INT16		100	0xFFFF = unsupported
60498	5011	20497				11	11 (P1)	11	1	INT16		100	0xFFFF = unsupported
60499	5012	20498				13	13 (P1)	13	1	INT16		100	0xFFFF = unsupported
60500	5013	20499				15	15 (P1)	15	1	INT16		100	0xFFFF = unsupported
60501	5014	20500				17	17 (P1)	17	1	INT16		100	0xFFFF = unsupported
60502	5015	20501				19	19 (P1)	19	1	INT16		100	0xFFFF = unsupported
60503	5016	20502				21	21 (P1)	21	1	INT16		100	0xFFFF = unsupported
60504	5017	20503				23	23 (P1)	23	1	INT16		100	0xFFFF = unsupported
60505	5018	20504				25	25 (P1)	25	1	INT16		100	0xFFFF = unsupported
60506	5019	20505				27	27 (P1)	27	1	INT16		100	0xFFFF = unsupported
60507	501A	20506				29	29 (P1)	29	1	INT16		100	0xFFFF = unsupported
60508	501B	20507				31	31 (P1)	31	1	INT16		100	0xFFFF = unsupported
60509	501C	20508				33	33 (P1)	33	1	INT16		100	0xFFFF = unsupported
60510	501D	20509				35	35 (P1)	35	1	INT16		100	0xFFFF = unsupported
60511	501E	20510				37	37 (P1)	37	1	INT16		100	0xFFFF = unsupported
60512	501F	20511				39	39 (P1)	39	1	INT16		100	0xFFFF = unsupported
60513	5020	20512				41	41 (P1)	41	1	INT16		100	0xFFFF = unsupported
60534	5035	20533				2	2 (P1)	2	1	INT16		100	0xFFFF = unsupported
60535	5036	20534				4	4 (P1)	4	1	INT16		100	0xFFFF = unsupported
60536	5037	20535				6	6 (P1)	6	1	INT16		100	0xFFFF = unsupported
60537	5038	20536				8	8 (P1)	8	1	INT16		100	0xFFFF = unsupported
60538	5039	20537				10	10 (P1)	10	1	INT16		100	0xFFFF = unsupported
60539	503A	20538				12	12 (P1)	12	1	INT16		100	0xFFFF = unsupported
60540	503B	20539				14	14 (P1)	14	1	INT16		100	0xFFFF = unsupported
60541	503C	20540				16	16 (P1)	16	1	INT16		100	0xFFFF = unsupported
60542	503D	20541				18	18 (P1)	18	1	INT16		100	0xFFFF = unsupported
60543	503E	20542				20	20 (P1)	20	1	INT16		100	0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60544	503F	20543				22	22 (P1)	22	1	INT16		100	0xFFFF = unsupported
60545	5040	20544				24	24 (P1)	24	1	INT16		100	0xFFFF = unsupported
60546	5041	20545				26	26 (P1)	26	1	INT16		100	0xFFFF = unsupported
60547	5042	20546				28	28 (P1)	28	1	INT16		100	0xFFFF = unsupported
60548	5043	20547				30	30 (P1)	30	1	INT16		100	0xFFFF = unsupported
60549	5044	20548				32	32 (P1)	32	1	INT16		100	0xFFFF = unsupported
60550	5045	20549				34	34 (P1)	34	1	INT16		100	0xFFFF = unsupported
60551	5046	20550				36	36 (P1)	36	1	INT16		100	0xFFFF = unsupported
60552	5047	20551				38	38 (P1)	38	1	INT16		100	0xFFFF = unsupported
60553	5048	20552				40	40 (P1)	40	1	INT16		100	0xFFFF = unsupported
60554	5049	20553				42	42 (P1)	42	1	INT16		100	0xFFFF = unsupported
60575	505E	20574				NA	1 (P2)	43	1	INT16		100	0xFFFF = unsupported
60576	505F	20575				NA	3 (P2)	45	1	INT16		100	0xFFFF = unsupported
60577	5060	20576				NA	5 (P2)	47	1	INT16		100	0xFFFF = unsupported
60578	5061	20577				NA	7 (P2)	49	1	INT16		100	0xFFFF = unsupported
60579	5062	20578				NA	9 (P2)	51	1	INT16		100	0xFFFF = unsupported
60580	5063	20579				NA	11 (P2)	53	1	INT16		100	0xFFFF = unsupported
60581	5064	20580				NA	13 (P2)	55	1	INT16		100	0xFFFF = unsupported
60582	5065	20581				NA	15 (P2)	57	1	INT16		100	0xFFFF = unsupported
60583	5066	20582				NA	17 (P2)	59	1	INT16		100	0xFFFF = unsupported
60584	5067	20583				NA	19 (P2)	61	1	INT16		100	0xFFFF = unsupported
60585	5068	20584				NA	21 (P2)	63	1	INT16		100	0xFFFF = unsupported
60586	5069	20585				NA	23 (P2)	65	1	INT16		100	0xFFFF = unsupported
60587	506A	20586				NA	25 (P2)	67	1	INT16		100	0xFFFF = unsupported
60588	506B	20587				NA	27 (P2)	69	1	INT16		100	0xFFFF = unsupported
60589	506C	20588				NA	29 (P2)	71	1	INT16		100	0xFFFF = unsupported
60590	506D	20589				NA	31 (P2)	73	1	INT16		100	0xFFFF = unsupported
60591	506E	20590				NA	33 (P2)	75	1	INT16		100	0xFFFF = unsupported
60592	506F	20591				NA	35 (P2)	77	1	INT16		100	0xFFFF = unsupported
60593	5070	20592				NA	37 (P2)	79	1	INT16		100	0xFFFF = unsupported
60594	5071	20593				NA	39 (P2)	81	1	INT16		100	0xFFFF = unsupported
60595	5072	20594				NA	41 (P2)	83	1	INT16		100	0xFFFF = unsupported
60616	5087	20615				NA	2 (P2)	44	1	INT16		100	0xFFFF = unsupported
60617	5088	20616				NA	4 (P2)	46	1	INT16		100	0xFFFF = unsupported
60618	5089	20617				NA	6 (P2)	48	1	INT16		100	0xFFFF = unsupported
60619	508A	20618				NA	8 (P2)	50	1	INT16		100	0xFFFF = unsupported
60620	508B	20619				NA	10 (P2)	52	1	INT16		100	0xFFFF = unsupported
60621	508C	20620				NA	12 (P2)	54	1	INT16		100	0xFFFF = unsupported
60622	508D	20621				NA	14 (P2)	56	1	INT16		100	0xFFFF = unsupported
60623	508E	20622				NA	16 (P2)	58	1	INT16		100	0xFFFF = unsupported
60624	508F	20623				NA	18 (P2)	60	1	INT16		100	0xFFFF = unsupported
60625	5090	20624				NA	20 (P2)	62	1	INT16		100	0xFFFF = unsupported
60626	5091	20625				NA	22 (P2)	64	1	INT16		100	0xFFFF = unsupported
60627	5092	20626				NA	24 (P2)	66	1	INT16		100	0xFFFF = unsupported
60628	5093	20627				NA	26 (P2)	68	1	INT16		100	0xFFFF = unsupported
60629	5094	20628				NA	28 (P2)	70	1	INT16		100	0xFFFF = unsupported
60630	5095	20629				NA	30 (P2)	72	1	INT16		100	0xFFFF = unsupported
60631	5096	20630				NA	32 (P2)	74	1	INT16		100	0xFFFF = unsupported
60632	5097	20631				NA	34 (P2)	76	1	INT16		100	0xFFFF = unsupported
60633	5098	20632				NA	36 (P2)	78	1	INT16		100	0xFFFF = unsupported
60634	5099	20633				NA	38 (P2)	80	1	INT16		100	0xFFFF = unsupported
60635	509A	20634				NA	40 (P2)	82	1	INT16		100	0xFFFF = unsupported
60636	509B	20635				NA	42 (P2)	84	1	INT16		100	0xFFFF = unsupported
60657	50B0	20656		RO	Current	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	84	UINT16			
60657	50B0	20656				1	1 (P1)	1	1	UINT16		10	Amperes, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60658	50B1	20657				3	3 (P1)	3	1	UINT16		10	Amperes, 0xFFFF = unsupported
60659	50B2	20658				5	5 (P1)	5	1	UINT16		10	Amperes, 0xFFFF = unsupported
60660	50B3	20659				7	7 (P1)	7	1	UINT16		10	Amperes, 0xFFFF = unsupported
60661	50B4	20660				9	9 (P1)	9	1	UINT16		10	Amperes, 0xFFFF = unsupported
60662	50B5	20661				11	11 (P1)	11	1	UINT16		10	Amperes, 0xFFFF = unsupported
60663	50B6	20662				13	13 (P1)	13	1	UINT16		10	Amperes, 0xFFFF = unsupported
60664	50B7	20663				15	15 (P1)	15	1	UINT16		10	Amperes, 0xFFFF = unsupported
60665	50B8	20664				17	17 (P1)	17	1	UINT16		10	Amperes, 0xFFFF = unsupported
60666	50B9	20665				19	19 (P1)	19	1	UINT16		10	Amperes, 0xFFFF = unsupported
60667	50BA	20666				21	21 (P1)	21	1	UINT16		10	Amperes, 0xFFFF = unsupported
60668	50BB	20667				23	23 (P1)	23	1	UINT16		10	Amperes, 0xFFFF = unsupported
60669	50BC	20668				25	25 (P1)	25	1	UINT16		10	Amperes, 0xFFFF = unsupported
60670	50BD	20669				27	27 (P1)	27	1	UINT16		10	Amperes, 0xFFFF = unsupported
60671	50BE	20670				29	29 (P1)	29	1	UINT16		10	Amperes, 0xFFFF = unsupported
60672	50BF	20671				31	31 (P1)	31	1	UINT16		10	Amperes, 0xFFFF = unsupported
60673	50C0	20672				33	33 (P1)	33	1	UINT16		10	Amperes, 0xFFFF = unsupported
60674	50C1	20673				35	35 (P1)	35	1	UINT16		10	Amperes, 0xFFFF = unsupported
60675	50C2	20674				37	37 (P1)	37	1	UINT16		10	Amperes, 0xFFFF = unsupported
60676	50C3	20675				39	39 (P1)	39	1	UINT16		10	Amperes, 0xFFFF = unsupported
60677	50C4	20676				41	41 (P1)	41	1	UINT16		10	Amperes, 0xFFFF = unsupported
60698	50D9	20697				2	2 (P1)	2	1	UINT16		10	Amperes, 0xFFFF = unsupported
60699	50DA	20698				4	4 (P1)	4	1	UINT16		10	Amperes, 0xFFFF = unsupported
60700	50DB	20699				6	6 (P1)	6	1	UINT16		10	Amperes, 0xFFFF = unsupported
60701	50DC	20700				8	8 (P1)	8	1	UINT16		10	Amperes, 0xFFFF = unsupported
60702	50DD	20701				10	10 (P1)	10	1	UINT16		10	Amperes, 0xFFFF = unsupported
60703	50DE	20702				12	12 (P1)	12	1	UINT16		10	Amperes, 0xFFFF = unsupported
60704	50DF	20703				14	14 (P1)	14	1	UINT16		10	Amperes, 0xFFFF = unsupported
60705	50E0	20704				16	16 (P1)	16	1	UINT16		10	Amperes, 0xFFFF = unsupported
60706	50E1	20705				18	18 (P1)	18	1	UINT16		10	Amperes, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60707	50E2	20706				20	20 (P1)	20	1	UINT16		10	Amperes, 0xFFFF = unsupported
60708	50E3	20707				22	22 (P1)	22	1	UINT16		10	Amperes, 0xFFFF = unsupported
60709	50E4	20708				24	24 (P1)	24	1	UINT16		10	Amperes, 0xFFFF = unsupported
60710	50E5	20709				26	26 (P1)	26	1	UINT16		10	Amperes, 0xFFFF = unsupported
60711	50E6	20710				28	28 (P1)	28	1	UINT16		10	Amperes, 0xFFFF = unsupported
60712	50E7	20711				30	30 (P1)	30	1	UINT16		10	Amperes, 0xFFFF = unsupported
60713	50E8	20712				32	32 (P1)	32	1	UINT16		10	Amperes, 0xFFFF = unsupported
60714	50E9	20713				34	34 (P1)	34	1	UINT16		10	Amperes, 0xFFFF = unsupported
60715	50EA	20714				36	36 (P1)	36	1	UINT16		10	Amperes, 0xFFFF = unsupported
60716	50EB	20715				38	38 (P1)	38	1	UINT16		10	Amperes, 0xFFFF = unsupported
60717	50EC	20716				40	40 (P1)	40	1	UINT16		10	Amperes, 0xFFFF = unsupported
60718	50ED	20717				42	42 (P1)	42	1	UINT16		10	Amperes, 0xFFFF = unsupported
60739	5102	20738				NA	1 (P2)	43	1	UINT16		10	Amperes, 0xFFFF = unsupported
60740	5103	20739				NA	3 (P2)	45	1	UINT16		10	Amperes, 0xFFFF = unsupported
60741	5104	20740				NA	5 (P2)	47	1	UINT16		10	Amperes, 0xFFFF = unsupported
60742	5105	20741				NA	7 (P2)	49	1	UINT16		10	Amperes, 0xFFFF = unsupported
60743	5106	20742				NA	9 (P2)	51	1	UINT16		10	Amperes, 0xFFFF = unsupported
60744	5107	20743				NA	11 (P2)	53	1	UINT16		10	Amperes, 0xFFFF = unsupported
60745	5108	20744				NA	13 (P2)	55	1	UINT16		10	Amperes, 0xFFFF = unsupported
60746	5109	20745				NA	15 (P2)	57	1	UINT16		10	Amperes, 0xFFFF = unsupported
60747	510A	20746				NA	17 (P2)	59	1	UINT16		10	Amperes, 0xFFFF = unsupported
60748	510B	20747				NA	19 (P2)	61	1	UINT16		10	Amperes, 0xFFFF = unsupported
60749	510C	20748				NA	21 (P2)	63	1	UINT16		10	Amperes, 0xFFFF = unsupported
60750	510D	20749				NA	23 (P2)	65	1	UINT16		10	Amperes, 0xFFFF = unsupported
60751	510E	20750				NA	25 (P2)	67	1	UINT16		10	Amperes, 0xFFFF = unsupported
60752	510F	20751				NA	27 (P2)	69	1	UINT16		10	Amperes, 0xFFFF = unsupported
60753	5110	20752				NA	29 (P2)	71	1	UINT16		10	Amperes, 0xFFFF = unsupported
60754	5111	20753				NA	31 (P2)	73	1	UINT16		10	Amperes, 0xFFFF = unsupported
60755	5112	20754				NA	33 (P2)	75	1	UINT16		10	Amperes, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60756	5113	20755				NA	35 (P2)	77	1	UINT16		10	Amperes, 0xFFFF = unsupported
60757	5114	20756				NA	37 (P2)	79	1	UINT16		10	Amperes, 0xFFFF = unsupported
60758	5115	20757				NA	39 (P2)	81	1	UINT16		10	Amperes, 0xFFFF = unsupported
60759	5116	20758				NA	41 (P2)	83	1	UINT16		10	Amperes, 0xFFFF = unsupported
60780	512B	20779				NA	2 (P2)	44	1	UINT16		10	Amperes, 0xFFFF = unsupported
60781	512C	20780				NA	4 (P2)	46	1	UINT16		10	Amperes, 0xFFFF = unsupported
60782	512D	20781				NA	6 (P2)	48	1	UINT16		10	Amperes, 0xFFFF = unsupported
60783	512E	20782				NA	8 (P2)	50	1	UINT16		10	Amperes, 0xFFFF = unsupported
60784	512F	20783				NA	10 (P2)	52	1	UINT16		10	Amperes, 0xFFFF = unsupported
60785	5130	20784				NA	12 (P2)	54	1	UINT16		10	Amperes, 0xFFFF = unsupported
60786	5131	20785				NA	14 (P2)	56	1	UINT16		10	Amperes, 0xFFFF = unsupported
60787	5132	20786				NA	16 (P2)	58	1	UINT16		10	Amperes, 0xFFFF = unsupported
60788	5133	20787				NA	18 (P2)	60	1	UINT16		10	Amperes, 0xFFFF = unsupported
60789	5134	20788				NA	20 (P2)	62	1	UINT16		10	Amperes, 0xFFFF = unsupported
60790	5135	20789				NA	22 (P2)	64	1	UINT16		10	Amperes, 0xFFFF = unsupported
60791	5136	20790				NA	24 (P2)	66	1	UINT16		10	Amperes, 0xFFFF = unsupported
60792	5137	20791				NA	26 (P2)	68	1	UINT16		10	Amperes, 0xFFFF = unsupported
60793	5138	20792				NA	28 (P2)	70	1	UINT16		10	Amperes, 0xFFFF = unsupported
60794	5139	20793				NA	30 (P2)	72	1	UINT16		10	Amperes, 0xFFFF = unsupported
60795	513A	20794				NA	32 (P2)	74	1	UINT16		10	Amperes, 0xFFFF = unsupported
60796	513B	20795				NA	34 (P2)	76	1	UINT16		10	Amperes, 0xFFFF = unsupported
60797	513C	20796				NA	36 (P2)	78	1	UINT16		10	Amperes, 0xFFFF = unsupported
60798	513D	20797				NA	38 (P2)	80	1	UINT16		10	Amperes, 0xFFFF = unsupported
60799	513E	20798				NA	40 (P2)	82	1	UINT16		10	Amperes, 0xFFFF = unsupported
60800	513F	20799				NA	42 (P2)	84	1	UINT16		10	Amperes, 0xFFFF = unsupported
60821	5154	20820		RO	Energy Usage	Breaker Position Panel 1x42	Breaker Position Panel 2x42	Breaker Position Panel 1x84	84	UINT32			
60821	5154	20820				1	1 (P1)	1	2	UINT32		10	kWh, 0xFFFF = unsupported
60823	5156	20822				3	3 (P1)	3	2	UINT32		10	kWh, 0xFFFF = unsupported
60825	5158	20824				5	5 (P1)	5	2	UINT32		10	kWh, 0xFFFF = unsupported
60827	515A	20826				7	7 (P1)	7	2	UINT32		10	kWh, 0xFFFF = unsupported
60829	515C	20828				9	9 (P1)	9	2	UINT32		10	kWh, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
60831	515E	20830				11	11 (P1)	11	2	UIN32		10	kWh, 0xFFFF = unsupported
60833	5160	20832				13	13 (P1)	13	2	UIN32		10	kWh, 0xFFFF = unsupported
60835	5162	20834				15	15 (P1)	15	2	UIN32		10	kWh, 0xFFFF = unsupported
60837	5164	20836				17	17 (P1)	17	2	UIN32		10	kWh, 0xFFFF = unsupported
60839	5166	20838				19	19 (P1)	19	2	UIN32		10	kWh, 0xFFFF = unsupported
60841	5168	20840				21	21 (P1)	21	2	UIN32		10	kWh, 0xFFFF = unsupported
60843	516A	20842				23	23 (P1)	23	2	UIN32		10	kWh, 0xFFFF = unsupported
60845	516C	20844				25	25 (P1)	25	2	UIN32		10	kWh, 0xFFFF = unsupported
60847	516E	20846				27	27 (P1)	27	2	UIN32		10	kWh, 0xFFFF = unsupported
60849	5170	20848				29	29 (P1)	29	2	UIN32		10	kWh, 0xFFFF = unsupported
60851	5172	20850				31	31 (P1)	31	2	UIN32		10	kWh, 0xFFFF = unsupported
60853	5174	20852				33	33 (P1)	33	2	UIN32		10	kWh, 0xFFFF = unsupported
60855	5176	20854				35	35 (P1)	35	2	UIN32		10	kWh, 0xFFFF = unsupported
60857	5178	20856				37	37 (P1)	37	2	UIN32		10	kWh, 0xFFFF = unsupported
60859	517A	20858				39	39 (P1)	39	2	UIN32		10	kWh, 0xFFFF = unsupported
60861	517C	20860				41	41 (P1)	41	2	UIN32		10	kWh, 0xFFFF = unsupported
60903	51A6	20902				2	2 (P1)	2	2	UIN32		10	kWh, 0xFFFF = unsupported
60905	51A8	20904				4	4 (P1)	4	2	UIN32		10	kWh, 0xFFFF = unsupported
60907	51AA	20906				6	6 (P1)	6	2	UIN32		10	kWh, 0xFFFF = unsupported
60909	51AC	20908				8	8 (P1)	8	2	UIN32		10	kWh, 0xFFFF = unsupported
60911	51AE	20910				10	10 (P1)	10	2	UIN32		10	kWh, 0xFFFF = unsupported
60913	51B0	20912				12	12 (P1)	12	2	UIN32		10	kWh, 0xFFFF = unsupported
60915	51B2	20914				14	14 (P1)	14	2	UIN32		10	kWh, 0xFFFF = unsupported
60917	51B4	20916				16	16 (P1)	16	2	UIN32		10	kWh, 0xFFFF = unsupported
60919	51B6	20918				18	18 (P1)	18	2	UIN32		10	kWh, 0xFFFF = unsupported
60921	51B8	20920				20	20 (P1)	20	2	UIN32		10	kWh, 0xFFFF = unsupported
60923	51BA	20922				22	22 (P1)	22	2	UIN32		10	kWh, 0xFFFF = unsupported
60925	51BC	20924				24	24 (P1)	24	2	UIN32		10	kWh, 0xFFFF = unsupported
60927	51BE	20926				26	26 (P1)	26	2	UIN32		10	kWh, 0xFFFF = unsupported
60929	51C0	20928				28	28 (P1)	28	2	UIN32		10	kWh, 0xFFFF = unsupported
60931	51C2	20930				30	30 (P1)	30	2	UIN32		10	kWh, 0xFFFF = unsupported
60933	51C4	20932				32	32 (P1)	32	2	UIN32		10	kWh, 0xFFFF = unsupported
60935	51C6	20934				34	34 (P1)	34	2	UIN32		10	kWh, 0xFFFF = unsupported
60937	51C8	20936				36	36 (P1)	36	2	UIN32		10	kWh, 0xFFFF = unsupported
60939	51CA	20938				38	38 (P1)	38	2	UIN32		10	kWh, 0xFFFF = unsupported
60941	51CC	20940				40	40 (P1)	40	2	UIN32		10	kWh, 0xFFFF = unsupported
60943	51CE	20942				42	42 (P1)	42	2	UIN32		10	kWh, 0xFFFF = unsupported
60985	51F8	20984				NA	1 (P2)	43	2	UIN32		10	kWh, 0xFFFF = unsupported
60987	51FA	20986				NA	3 (P2)	45	2	UIN32		10	kWh, 0xFFFF = unsupported
60989	51FC	20988				NA	5 (P2)	47	2	UIN32		10	kWh, 0xFFFF = unsupported
60991	51FE	20990				NA	7 (P2)	49	2	UIN32		10	kWh, 0xFFFF = unsupported
60993	5200	20992				NA	9 (P2)	51	2	UIN32		10	kWh, 0xFFFF = unsupported
60995	5202	20994				NA	11 (P2)	53	2	UIN32		10	kWh, 0xFFFF = unsupported
60997	5204	20996				NA	13 (P2)	55	2	UIN32		10	kWh, 0xFFFF = unsupported
60999	5206	20998				NA	15 (P2)	57	2	UIN32		10	kWh, 0xFFFF = unsupported
61001	5208	21000				NA	17 (P2)	59	2	UIN32		10	kWh, 0xFFFF = unsupported
61003	520A	21002				NA	19 (P2)	61	2	UIN32		10	kWh, 0xFFFF = unsupported
61005	520C	21004				NA	21 (P2)	63	2	UIN32		10	kWh, 0xFFFF = unsupported
61007	520E	21006				NA	23 (P2)	65	2	UIN32		10	kWh, 0xFFFF = unsupported
61009	5210	21008				NA	25 (P2)	67	2	UIN32		10	kWh, 0xFFFF = unsupported
61011	5212	21010				NA	27 (P2)	69	2	UIN32		10	kWh, 0xFFFF = unsupported
61013	5214	21012				NA	29 (P2)	71	2	UIN32		10	kWh, 0xFFFF = unsupported
61015	5216	21014				NA	31 (P2)	73	2	UIN32		10	kWh, 0xFFFF = unsupported
61017	5218	21016				NA	33 (P2)	75	2	UIN32		10	kWh, 0xFFFF = unsupported
61019	521A	21018				NA	35 (P2)	77	2	UIN32		10	kWh, 0xFFFF = unsupported
61021	521C	21020				NA	37 (P2)	79	2	UIN32		10	kWh, 0xFFFF = unsupported
61023	521E	21022				NA	39 (P2)	81	2	UIN32		10	kWh, 0xFFFF = unsupported
61025	5220	21024				NA	41 (P2)	83	2	UIN32		10	kWh, 0xFFFF = unsupported

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
61067	524A	21066				NA	2 (P2)	44	2	UINT32		10	kWh, 0xFFFF = unsupported
61069	524C	21068				NA	4 (P2)	46	2	UINT32		10	kWh, 0xFFFF = unsupported
61071	524E	21070				NA	6 (P2)	48	2	UINT32		10	kWh, 0xFFFF = unsupported
61073	5250	21072				NA	8 (P2)	50	2	UINT32		10	kWh, 0xFFFF = unsupported
61075	5252	21074				NA	10 (P2)	52	2	UINT32		10	kWh, 0xFFFF = unsupported
61077	5254	21076				NA	12 (P2)	54	2	UINT32		10	kWh, 0xFFFF = unsupported
61079	5256	21078				NA	14 (P2)	56	2	UINT32		10	kWh, 0xFFFF = unsupported
61081	5258	21080				NA	16 (P2)	58	2	UINT32		10	kWh, 0xFFFF = unsupported
61083	525A	21082				NA	18 (P2)	60	2	UINT32		10	kWh, 0xFFFF = unsupported
61085	525C	21084				NA	20 (P2)	62	2	UINT32		10	kWh, 0xFFFF = unsupported
61087	525E	21086				NA	22 (P2)	64	2	UINT32		10	kWh, 0xFFFF = unsupported
61089	5260	21088				NA	24 (P2)	66	2	UINT32		10	kWh, 0xFFFF = unsupported
61091	5262	21090				NA	26 (P2)	68	2	UINT32		10	kWh, 0xFFFF = unsupported
61093	5264	21092				NA	28 (P2)	70	2	UINT32		10	kWh, 0xFFFF = unsupported
61095	5266	21094				NA	30 (P2)	72	2	UINT32		10	kWh, 0xFFFF = unsupported
61097	5268	21096				NA	32 (P2)	74	2	UINT32		10	kWh, 0xFFFF = unsupported
61099	526A	21098				NA	34 (P2)	76	2	UINT32		10	kWh, 0xFFFF = unsupported
61101	526C	21100				NA	36 (P2)	78	2	UINT32		10	kWh, 0xFFFF = unsupported
61103	526E	21102				NA	38 (P2)	80	2	UINT32		10	kWh, 0xFFFF = unsupported
61105	5270	21104				NA	40 (P2)	82	2	UINT32		10	kWh, 0xFFFF = unsupported
61107	5272	21106				NA	42 (P2)	84	2	UINT32		10	kWh, 0xFFFF = unsupported
	529C	21148											
	<b>7530</b>	<b>30000</b>	<b>4.3 Alarm/Status Registers</b>										
<b>70001</b>	<b>7530</b>	<b>30000</b>	<b>Bit</b>	<b>RO</b>	<b>Branch Current Alarm Register</b>	<b>Breaker Position Panel 1x42</b>	<b>Breaker Position Panel 2x42</b>	<b>Breaker Position Panel 1x84</b>	<b>84</b>	<b>UINT16 (bool x 16)</b>			
			<b>0</b>		<b>Current Maximum alarm</b>					<b>BOOLEAN</b>			<b>0 = No Alarm ; 1 = Alarm Active</b>
			<b>1</b>		<b>Current Minimum alarm</b>								<b>0 = No Alarm ; 1 = Alarm Active</b>
			<b>2</b>		<b>Current High alarm</b>								<b>0 = No Alarm ; 1 = Alarm Active</b>
			<b>3</b>		<b>Current Low alarm</b>								<b>0 = No Alarm ; 1 = Alarm Active</b>
			<b>4</b>		<b>NA</b>								
			<b>5</b>		<b>NA</b>								
			<b>6</b>		<b>NA</b>								
			<b>7</b>		<b>NA</b>								
			<b>8</b>		<b>NA</b>								
			<b>9</b>		<b>NA</b>								
			<b>10</b>		<b>NA</b>								
			<b>11</b>		<b>NA</b>								
			<b>12</b>		<b>NA</b>								
			<b>13</b>		<b>NA</b>								
			<b>14</b>		<b>NA</b>								
			<b>15</b>		<b>NA</b>								
70001	7530	30000				1	1 (P1)	1	1				
70002	7531	30001				3	3 (P1)	3	1				
70003	7532	30002				5	5 (P1)	5	1				
70004	7533	30003				7	7 (P1)	7	1				
70005	7534	30004				9	9 (P1)	9	1				
70006	7535	30005				11	11 (P1)	11	1				
70007	7536	30006				13	13 (P1)	13	1				
70008	7537	30007				15	15 (P1)	15	1				
70009	7538	30008				17	17 (P1)	17	1				
70010	7539	30009				19	19 (P1)	19	1				
70011	753A	30010				21	21 (P1)	21	1				



Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
70012	753B	30011				23	23 (P1)	23	1				
70013	753C	30012				25	25 (P1)	25	1				
70014	753D	30013				27	27 (P1)	27	1				
70015	753E	30014				29	29 (P1)	29	1				
70016	753F	30015				31	31 (P1)	31	1				
70017	7540	30016				33	33 (P1)	33	1				
70018	7541	30017				35	35 (P1)	35	1				
70019	7542	30018				37	37 (P1)	37	1				
70020	7543	30019				39	39 (P1)	39	1				
70021	7544	30020				41	41 (P1)	41	1				
70042	7559	30041				2	2 (P1)	2	1				
70043	755A	30042				4	4 (P1)	4	1				
70044	755B	30043				6	6 (P1)	6	1				
70045	755C	30044				8	8 (P1)	8	1				
70046	755D	30045				10	10 (P1)	10	1				
70047	755E	30046				12	12 (P1)	12	1				
70048	755F	30047				14	14 (P1)	14	1				
70049	7560	30048				16	16 (P1)	16	1				
70050	7561	30049				18	18 (P1)	18	1				
70051	7562	30050				20	20 (P1)	20	1				
70052	7563	30051				22	22 (P1)	22	1				
70053	7564	30052				24	24 (P1)	24	1				
70054	7565	30053				26	26 (P1)	26	1				
70055	7566	30054				28	28 (P1)	28	1				
70056	7567	30055				30	30 (P1)	30	1				
70057	7568	30056				32	32 (P1)	32	1				
70058	7569	30057				34	34 (P1)	34	1				
70059	756A	30058				36	36 (P1)	36	1				
70060	756B	30059				38	38 (P1)	38	1				
70061	756C	30060				40	40 (P1)	40	1				
70062	756D	30061				42	42 (P1)	42	1				
70083	7582	30082				NA	1 (P2)	43	1				
70084	7583	30083				NA	3 (P2)	45	1				
70085	7584	30084				NA	5 (P2)	47	1				
70086	7585	30085				NA	7 (P2)	49	1				
70087	7586	30086				NA	9 (P2)	51	1				
70088	7587	30087				NA	11 (P2)	53	1				
70089	7588	30088				NA	13 (P2)	55	1				
70090	7589	30089				NA	15 (P2)	57	1				
70091	758A	30090				NA	17 (P2)	59	1				
70092	758B	30091				NA	19 (P2)	61	1				
70093	758C	30092				NA	21 (P2)	63	1				
70094	758D	30093				NA	23 (P2)	65	1				
70095	758E	30094				NA	25 (P2)	67	1				
70096	758F	30095				NA	27 (P2)	69	1				
70097	7590	30096				NA	29 (P2)	71	1				
70098	7591	30097				NA	31 (P2)	73	1				
70099	7592	30098				NA	33 (P2)	75	1				
70100	7593	30099				NA	35 (P2)	77	1				
70101	7594	30100				NA	37 (P2)	79	1				
70102	7595	30101				NA	39 (P2)	81	1				
70103	7596	30102				NA	41 (P2)	83	1				
70124	75AB	30123				NA	2 (P2)	44	1				
70125	75AC	30124				NA	4 (P2)	46	1				
70126	75AD	30125				NA	6 (P2)	48	1				
70127	75AE	30126				NA	8 (P2)	50	1				
70128	75AF	30127				NA	10 (P2)	52	1				
70129	75B0	30128				NA	12 (P2)	54	1				

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
70130	75B1	30129				NA	14 (P2)	56	1				
70131	75B2	30130				NA	16 (P2)	58	1				
70132	75B3	30131				NA	18 (P2)	60	1				
70133	75B4	30132				NA	20 (P2)	62	1				
70134	75B5	30133				NA	22 (P2)	64	1				
70135	75B6	30134				NA	24 (P2)	66	1				
70136	75B7	30135				NA	26 (P2)	68	1				
70137	75B8	30136				NA	28 (P2)	70	1				
70138	75B9	30137				NA	30 (P2)	72	1				
70139	75BA	30138				NA	32 (P2)	74	1				
70140	75BB	30139				NA	34 (P2)	76	1				
70141	75BC	30140				NA	36 (P2)	78	1				
70142	75BD	30141				NA	38 (P2)	80	1				
70143	75BE	30142				NA	40 (P2)	82	1				
70144	75BF	30143				NA	42 (P2)	84	1				
<b>70165</b>	<b>75D4</b>	<b>30164</b>	<b>Bit</b>	<b>RO</b>	<b>Branch Power Alarm Register</b>	<b>Breaker Position Panel 1x42</b>	<b>Breaker Position Panel 2x42</b>	<b>Breaker Position Panel 1x84</b>	<b>84</b>	<b>UINT16 (bool x 16)</b>			
			<b>0</b>		<b>Under kVA</b>					<b>BOOLEAN</b>			<b>0 = No Alarm 1 = Alarm Active</b>
			<b>1</b>		<b>Over kVA</b>								<b>0 = No Alarm 1 = Alarm Active</b>
			<b>2</b>		<b>NA</b>								
			<b>3</b>		<b>NA</b>								
			<b>4</b>		<b>NA</b>								
			<b>5</b>		<b>NA</b>								
			<b>6</b>		<b>NA</b>								
			<b>7</b>		<b>NA</b>								
			<b>8</b>		<b>NA</b>								
			<b>9</b>		<b>NA</b>								
			<b>10</b>		<b>NA</b>								
			<b>11</b>		<b>NA</b>								
			<b>12</b>		<b>NA</b>								
			<b>13</b>		<b>NA</b>								
			<b>14</b>		<b>NA</b>								
			<b>15</b>		<b>NA</b>								
70165	75D4	30164				1	1 (P1)	1	1				
70166	75D5	30165				3	3 (P1)	3	1				
70167	75D6	30166				5	5 (P1)	5	1				
70168	75D7	30167				7	7 (P1)	7	1				
70169	75D8	30168				9	9 (P1)	9	1				
70170	75D9	30169				11	11 (P1)	11	1				
70171	75DA	30170				13	13 (P1)	13	1				
70172	75DB	30171				15	15 (P1)	15	1				
70173	75DC	30172				17	17 (P1)	17	1				
70174	75DD	30173				19	19 (P1)	19	1				
70175	75DE	30174				21	21 (P1)	21	1				
70176	75DF	30175				23	23 (P1)	23	1				
70177	75E0	30176				25	25 (P1)	25	1				
70178	75E1	30177				27	27 (P1)	27	1				
70179	75E2	30178				29	29 (P1)	29	1				
70180	75E3	30179				31	31 (P1)	31	1				
70181	75E4	30180				33	33 (P1)	33	1				
70182	75E5	30181				35	35 (P1)	35	1				
70183	75E6	30182				37	37 (P1)	37	1				
70184	75E7	30183				39	39 (P1)	39	1				

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
70185	75E8	30184				41	41 (P1)	41	1				
70206	75FD	30205				2	2 (P1)	2	1				
70207	75FE	30206				4	4 (P1)	4	1				
70208	75FF	30207				6	6 (P1)	6	1				
70209	7600	30208				8	8 (P1)	8	1				
70210	7601	30209				10	10 (P1)	10	1				
70211	7602	30210				12	12 (P1)	12	1				
70212	7603	30211				14	14 (P1)	14	1				
70213	7604	30212				16	16 (P1)	16	1				
70214	7605	30213				18	18 (P1)	18	1				
70215	7606	30214				20	20 (P1)	20	1				
70216	7607	30215				22	22 (P1)	22	1				
70217	7608	30216				24	24 (P1)	24	1				
70218	7609	30217				26	26 (P1)	26	1				
70219	760A	30218				28	28 (P1)	28	1				
70220	760B	30219				30	30 (P1)	30	1				
70221	760C	30220				32	32 (P1)	32	1				
70222	760D	30221				34	34 (P1)	34	1				
70223	760E	30222				36	36 (P1)	36	1				
70224	760F	30223				38	38 (P1)	38	1				
70225	7610	30224				40	40 (P1)	40	1				
70226	7611	30225				42	42 (P1)	42	1				
70247	7626	30246				NA	1 (P2)	43	1				
70248	7627	30247				NA	3 (P2)	45	1				
70249	7628	30248				NA	5 (P2)	47	1				
70250	7629	30249				NA	7 (P2)	49	1				
70251	762A	30250				NA	9 (P2)	51	1				
70252	762B	30251				NA	11 (P2)	53	1				
70253	762C	30252				NA	13 (P2)	55	1				
70254	762D	30253				NA	15 (P2)	57	1				
70255	762E	30254				NA	17 (P2)	59	1				
70256	762F	30255				NA	19 (P2)	61	1				
70257	7630	30256				NA	21 (P2)	63	1				
70258	7631	30257				NA	23 (P2)	65	1				
70259	7632	30258				NA	25 (P2)	67	1				
70260	7633	30259				NA	27 (P2)	69	1				
70261	7634	30260				NA	29 (P2)	71	1				
70262	7635	30261				NA	31 (P2)	73	1				
70263	7636	30262				NA	33 (P2)	75	1				
70264	7637	30263				NA	35 (P2)	77	1				
70265	7638	30264				NA	37 (P2)	79	1				
70266	7639	30265				NA	39 (P2)	81	1				
70267	763A	30266				NA	41 (P2)	83	1				
70288	764F	30287				NA	2 (P2)	44	1				
70289	7650	30288				NA	4 (P2)	46	1				
70290	7651	30289				NA	6 (P2)	48	1				
70291	7652	30290				NA	8 (P2)	50	1				
70292	7653	30291				NA	10 (P2)	52	1				
70293	7654	30292				NA	12 (P2)	54	1				
70294	7655	30293				NA	14 (P2)	56	1				
70295	7656	30294				NA	16 (P2)	58	1				
70296	7657	30295				NA	18 (P2)	60	1				
70297	7658	30296				NA	20 (P2)	62	1				
70298	7659	30297				NA	22 (P2)	64	1				
70299	765A	30298				NA	24 (P2)	66	1				
70300	765B	30299				NA	26 (P2)	68	1				
70301	765C	30300				NA	28 (P2)	70	1				
70302	765D	30301				NA	30 (P2)	72	1				

Modicon Standard Register Number	Absolute Starting Register Address, (Hexadecimal)	Absolute Starting Register Address, (Decimal)	Bit	Access (RO/RW)	Data Point	Panel 1x42	Panel 2x42	Panel 1x84	Length # registers	Type	Multiply Reading By:	Divide Reading By:	Valid Response
70303	765E	30302				NA	32 (P2)	74	1				
70304	765F	30303				NA	34 (P2)	76	1				
70305	7660	30304				NA	36 (P2)	78	1				
70306	7661	30305				NA	38 (P2)	80	1				
70307	7662	30306				NA	40 (P2)	82	1				
70308	7663	30307				NA	42 (P2)	84	1				
	7678	30328											

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