

SERIES 500

Model 575/565, Surge Protective Device

Guide Specifications

1.0 GENERAL

1.1 Summary : These specifications describe the electrical and mechanical requirements for a modular, high-energy surge protective device (SPD) that meets the Motorola R56 standard. The specified system shall provide effective, high-energy surge current diversion and be suitable for use as Type 1, 20kA device per ANSI/UL 1449 Fourth Edition.

1.2 Standards : The specified system shall be designed, manufactured, tested and installed in compliance with the following codes and standards:

Motorola R56 2005 Version, Standard and Guidelines for Communications Sites
Underwriters Laboratories; ANSI/UL 1449 4th Edition 2014 Revision
Underwriters Laboratories; UL 1283 6th Edition 2015 (complimentary listing for Type 2 locations)
Canadian Underwriters Laboratories (cUL)
American National Standards Institute and Institute of Electrical and Electronic Engineers (ANSI/IEEE C62.34, C62.41, C62.45)
Institute of Electrical and Electronic Engineers 1100 Emerald Book
Federal Information Processing Standards Publication 94 (FIPS PUB 94)
National Fire Protection Association (NFPA 20, 70, 75 and 780)
International Standards Organization (ISO) Company certified ISO 9001 for manufacturing, design and service
Federal Communications Commission (FCC)

The system shall be UL listed and labeled under ANSI/UL 1449 Fourth Edition and the voltage protection ratings (VPRs) shall be permanently affixed to the SPD.

1.3 System Description: The Motorola R56 Type 1 SPD (ASCO Model 575) shall be constructed using a primary and secondary stage hybrid transient suppression system. The primary stage shall be constructed utilizing silicon avalanche diode (SAD) technology. The SAD module shall not “self-sacrifice” to protect the system; rather it shall provide immediate transition to the secondary stage when subjected to potentially harmful levels. The secondary stage shall be constructed using multiple surge current diversion modules utilizing metal oxide varistors (MOV) computer matched to a variance of ± 1 volt and tested for manufacturing defects. The modules shall be designed and constructed in a manner that ensures surge current sharing. Use of gas tubes, silicon avalanche diodes or selenium cells are unacceptable unless documentation from a nationally recognized laboratory demonstrates current sharing of all dissimilar components at all surge current levels.

The Motorola R56 Type 2 SPD (ASCO Model 565) shall be constructed using multiple surge current diversion modules utilizing metal oxide varistors (MOV) computer matched to a variance of ± 1 volt and tested for manufacturing defects. The modules shall be designed and constructed in a manner that ensures surge current sharing. Use of gas tubes, silicon avalanche diodes or selenium cells are unacceptable unless documentation from a nationally recognized laboratory demonstrates current sharing of all dissimilar components at all surge current levels.

1.4 Electrical Requirements (selection required)

1.4.1 Nominal system operating voltage

120/240VAC, Single Phase, 3 Wire plus Ground
120/208VAC, 3 Phase Wye, 4 Wire plus Ground

1.4.2 Maximum Continuous Operating Voltage (MCOV): The SPD and all components in the suppression path (including all current diversion components) maximum continuous operating

voltage (MCOV) shall be greater than 115% of the nominal system operating voltage to ensure the ability of the system to withstand temporary RMS over-voltage (swell conditions).

1.4.3 Operating Frequency: The operating frequency range of the system shall be 47-63 Hz.

1.5 Life Cycle Testing: The SPD shall be duty life cycle tested to withstand 10kA (8x20µs), 20kV (1.2x50µs), IEEE C62.41 Category C surge current with less than 5% degradation of clamping voltage. The minimum numbers of surges the unit shall be able to protect against are 12,000 Life Cycle Surges per Mode and per Phase.

1.6 Overcurrent Protection: Fusing: All suppression components shall be individually fused and rated to allow maximum specified surge current capacity. For every 100k amps of surge current capacity, 120 amps RMS of internal, integral fusing shall be required. Devices that utilize a single fuse to protect two or more suppression paths are not excepted. Individual surge components shall be sand packed and shall be UL listed to be capable of interrupting up to 65 kA symmetrical fault current with 480 VAC applied. Replaceable fusing is unacceptable. Overcurrent protection that limits specified surge currents is not acceptable.

1.7 Performance Ratings

1.7.1 Surge Current Capacity:

Motorola R56 Type 1 SPD

160kA MOV Surge Rating per Mode (L-N), 20kA SAD Surge Rating per Mode (L-N)

160kA MOV Surge Rating per Phase, 20kA SAD Surge Rating per Phase

Motorola R56 Type 2 SPD

160kA MOV Surge Rating per Mode (L-N)

160kA MOV Surge Rating per Phase

1.8 Design Requirements

1.8.1 Noise Attenuation: The filter shall provide an attenuation of 63 db max from 10 kHz to 100MHz, per 50 Ohm Insertion Loss Methodology from MIL 220A. The system shall provide up to 120-dB insertion loss from 100 kHz to 100 MHz when used in a coordinated facility system

1.8.2 Protection Modes: The SPD shall provide protection as follows: Line to Neutral (L-N) and Line to Line (L-L)

1.8.3 ANSI/UL 1449 Voltage Protection Ratings: The maximum UL 1449 listed surge ratings for each and/or all of the specified protection modes shall not exceed the following:

System Voltage	ANSI/UL 1449 Fourth Edition VPR	
	L-N	L-L
120/208, 120/240	600V	1000V
277/480	1000V	1800V

1.8.4 ANSI/UL 1449 Nominal Discharge Current: The ANSI/UL 1449 Nominal Discharge Current Rating shall be a minimum of 20kA.

1.9 Submittals

1.9.1 Documentation: These specifications are based on the ASCO Series 575 & 565 Motorola R56 Approved devices. All other manufacturers shall submit for 10-day pre-approval, a detailed compliance or exception statements to all provisions of this specification to allow consideration. Additionally, manufacturers shall submit test data verifying the following: life cycle testing, overcurrent protection, UL1449 Fourth edition, Motorola R56 approval, noise attenuation and

surge current capacity. Failure to do so will result in product disapproval. Any deviation from the published specification will result in an applicable deduct applied.

- 1.9.2 Equipment Manual:** The manufacturer shall furnish an installation manual with installation, startup, and operating instructions for the specified system.
- 1.9.3 Drawings:** Electrical and mechanical drawings shall be provided by the manufacturer that show unit dimensions, weights, component and connection locations, mounting provisions, connection details and wiring diagram.
- 1.10 Quality Assurance:** The manufacturer shall be ISO 9001:2000 certified. The specified interconnect assembly shall be designed and manufactured in the USA by a qualified manufacturer of SPD products and line conditioning equipment. The manufacturer shall have been engaged in the design and manufacturer of such products for a minimum of 20 years.
- 1.11 Environmental Requirements**
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|------------------------|-------------------------------------------|
| Storage Temperature: | -55 to +85°C (-67 to +185°F) |
| Operating Temperature: | -40 to +60°C (-40 to +140°F) |
| Relative Humidity: | 0% to 95% (non-condensing) |
| Audible Noise: | less than 45dB at 5 feet (1.5m) |
| Operating Altitude: | 0 to 18,000 feet (5,486m) above sea level |
- 1.12 Warranty:** The manufacturer shall provide a full ten-year parts and five-year labor warranty from date of shipment against any part failure when installed in compliance with manufacturer's written instructions, UL Listing requirements and any applicable national, state or local electrical codes. Direct factory trained, ISO 9001:2000 certified employees must be available for 48-hour assessment. A 24-hour 800 number must be available to support warranty.

2.0 PRODUCTS

- 2.1 Enclosure:** The specified system shall be provided in a NEMA 4X weather proof plastic enclosure with no ventilation openings. The cover of the enclosure shall be hinged on the left side and require a tool for access to internal components. All monitoring indication must be visible without opening the door. The enclosure maximum dimensions shall be 16 in. (406 mm) high x 14 in. (355 mm) wide x 8 in. (203 mm) deep.
- 2.2 Connections:** The terminals shall be provided to accommodate wire sizes up to #14-2/O.
- 2.3 Internal Connections and Serviceability:** All surge current diversion module connections shall be by way of low-impedance copper plates. Surge current diversion modules shall use bolted connections to the plates for reliable, low-impedance connections. The system shall be designed for easy servicing by a qualified field electrician, providing simple change out of any or all SPD modules. Designs that require factory service are not acceptable. Any unit using "plug-in" type modules are not acceptable. All connections, conductors and terminals must be appropriately sized for specified surge current capacity.
- 2.4 Standard Features:**
- 2.4.1 Unit Testing Capability**—The SPD monitoring circuitry must continually verify the protection status during operation and display this information visible from the front of the enclosure.
- 2.4.2 Unit Status Indicators**—SPD shall be equipped with red and green solid state indicators mounted within the enclosure and must be externally visible.
- 2.4.3 Dry Contacts for Remote Monitoring**—SPD must have a minimum of 2 sets of electrically isolated Form C dry contacts, one normally open and one normally closed.
- 2.4.4 Undervoltage Detection**—SPD shall be equipped with 70% undervoltage detection.

2.4.5 Phase Loss Monitoring—SPD shall be equipped with phase loss monitoring.

2.4.6 Power Loss Monitoring—SPD shall be equipped with power loss monitoring.

2.5 Testing

2.5.1 Component Testing and Monitoring: The proposed product shall be single pulsed surge current tested in all modes at the rated surge currents by an industry recognized independent test laboratory. The test shall include a surge impulse (6kV [1.2x50µs], 500 amp [8x20 µs] waveform) to benchmark the unit's suppression voltage. The applied impulse is followed by a single pulse surge of the maximum rated surge current magnitude, followed by a second 6kV [1.2x50µs], 500 amp [8x20 µs] impulse as a means of measuring clamping deviation (component degradation). Compliance is achieved if the two measured suppression voltage do not vary by more than 5%.

3.0 EXECUTION

3.1 Installation: The installing contractor shall install the parallel SPD with short and straight conductors as practically possible. The contractor shall twist the SPD input conductors together to reduce input conductor inductance. The contractor shall follow the SPD manufacturer's recommended installation practices as found in the installation, operation and maintenance manual and comply with all applicable codes.

These Guide Specifications comply with the outlines of the Construction Specification Institute per CSI MP-2-2-85 and MP-2-1-88