

**TSXH5724M / TSXH5744M
Firmware history**

Version	History		
	OPR/PCR	bFO	Description
V3.20 (11/19)	PEP0515565R		Improvement of cyber security protection
	PEP0523291R		
	PEP0510526R		
	PEP0515605R		
	PEP0532540R		
	PEP0548240R		
	PEP0543772R		
	PEP0543773R		
	PEP0543774R		
	PEP0543776R		
	PEP0544419R		

Version	History		
	OPR/PCR	Vantive case or internal PR	Description
V3.0	PEP0047851R		Fix DiagViewer that does not display messages and symbols for instances bigger than 254 in a single section

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	OPR/PCR	Vantive case or internal PR	Description
V2.83	PEP0015710R		When ETY modules are positioned in the racks in slot number higher than 7, they boot up in correct modes at power up.
	PEP0035536R		Improve high frequency Swap robustness
	PEP0036611R		Fix bug regarding bumps on in rack outputs
	PEP0038231R		Fix a bug on %SW30 value when the PLC is in Standby mode
	PEP0039458R		Swap robustness enhancement
	PEP0040206R		Fix bug regarding sporadic reset of reverse registers when swapping
	PEP0011918R		Improve the robustness of a CPU which application implements a "CANCEL" EF in a FAST task.
	PEP0021251R		TSXSCY21601 modules that are configured in Modbus Master mode and use a communication EF will reject slave addresses greater than 98
		UNITY00047500	Improve performance of data Dictionary Browse requests coming from HMI
	20045482		Addition of two new EF for data storage of unallocated variables in a PCMCIA card (EF READ V PCMCIA and WRITE V PCMCIA)
		UNITY00046864	Fix bug regarding the FREERUN EF behaviour during Warm Start
		UNITY00049521	Fix on %S75 value when PCMCIA battery slot 1 is in error
		UNITY00049593	Improve behaviour of a SCP114 card located in the CPU during warm start
		UNITY00045340	Reset activity bit of communication EFs when the addressed module is not present in the system.
		UNITY00050007	Improve the robustness of CPU communication kernel
	PEP0030352R		CPUs answer correctly to Modbus FC05 "Write Single Coil" request
	PEP0013128R	1081706	Improve the robustness of a CPU against Scada wrong requests from OFS
	PEP0014351R	994155	Gives the capability to configure a cold start at each power on cycle
	PEP0015477R	1046800	Fix a bug when using a memory size for structured type instances greater than 1Mbytes
	PEP0015537R	1121176	Behaviour enhancement when several PC are connected to a single PLC
	20038551	947690 1018538	Improve restart robustness of FIPIO STBNFP2212 modules after power cycle
	20040883	UNITY00036615	When TSXDMY* modules are placed on a distant rack, improve their behaviours when the CPU rack is restarted.
	20041411	1003716	Performance improvement for ModbusPlus communication for application with a long cycle time.
	20041687	UNITY00040623	Improve Warm Start behaviour when using the "automatic start in Run" option
	20041881	UNITY00040619	Fix a bug for variable animation using a HMI or a Web server when performing Online Modification on the application embedded in the PLC
	20042789	UNITY00043353 1035075 1102080	Behaviour improvement of the system when managing more than 340 analog channels
20043558	UNITY00043418	Improvement of PLC Screen information on online configuration change when Mast and Fast tasks are mixed	

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V2.5			ORIGINAL VERSION
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